AN INFINITE 2-DIMENSIONAL ARRAY ASSOCIATED WITH ELECTRIC CIRCUITS

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ABSTRACT. Except for Koshy who devotes seven pages to applications of Fibonacci Numbers to electric circuits, most books and the Fibonacci Quarterly have been relatively silent on applications of graphs and electric circuits to Fibonacci numbers. This paper continues a recent trend of papers studying the interplay of graphs, circuits, and Fibonacci numbers by presenting and studying the Circuit Array, an infinite 2-dimensional array whose entries are electric resistances labeling edge values of circuits associated with a family of graphs. The Circuit Array has several features distinguishing it from other more familiar arrays such as the Binomial Array and Wythoff Array. For example, it can be proven modulo a strongly supported conjecture that the numerators of its left-most diagonal do not satisfy any linear, homogeneous, recursion, with constant coefficients (LHRCC). However, we conjecture with supporting numerical evidence an asymptotic formula involving π satisfied by the left-most diagonal of the Circuit Array.

1. Electrical Circuits, Linear 2-trees, and Fibonacci Numbers

Koshy [7, pp. 43-49] lists applications of electrical circuits yielding interesting Fibonacci identities. However, aside from this, most books and as well as the issues of the Fibonacci Quarterly have been mostly silent on this application.

To begin our review of the recent literature, which has renewed interest in this application, first, recall that one modern graph metric, effective resistance, requires that the graph be represented as an electric circuit with edges in the graph represented by resistors. This idea is illustrated in Figure 1.

Several papers [1, 2] have explored effective resistances in electrical circuits whose underlying graphs are so-called linear 2-trees. In addition to showing that these effective resistances are rational functions of Fibonacci numbers, these circuits naturally give rise to interesting and new Fibonacci identities. For example the identities

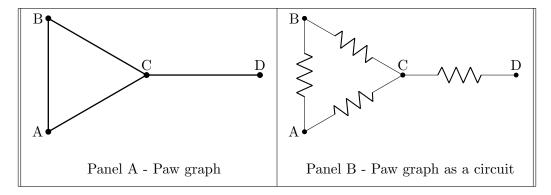


FIGURE 1. Illustration of a graph and its associated circuit.

$$\sum_{i=1}^{m} \frac{F_i F_{i+1}}{L_i L_{i+1}} = \frac{(m+1)L_{m+1} - F_{m+1}}{5L_{m+1}}, \quad \text{for } m \ge 1,$$
(1.1)

and for k = 3, 4, ..., n - 2,

$$\sum_{j=3}^{k} \left[(-1)^{j} F_{n-2j+1} (F_n + F_{j-2} F_{n-j-1}) \right] = -F_{k-2} F_{k+1} F_{n-k-2} F_{n+1-k}. \tag{1.2}$$

To appreciate these recent contributions we provide additional background. Effective resistance, also termed resistance distance in the literature, is a graph metric whose definition was motivated by the consideration of a graph as an electrical circuit. More formally, given a graph, we determine the effective resistance between any two vertices in that graph by assuming that the graph represents an electrical circuit with resistances on each edge. Given any two vertices labeled i and j, for convenience assume that one unit of current flows into vertex i and one unit of current flows out of vertex j. The potential difference $v_i - v_j$ between nodes i and j needed to maintain this current is the effective resistance between i and j. Figure 1 illustrates this.

Recent prior works [1, 2, 3] study effective resistance in a class of graphs termed *linear* 2-trees, also known as 2-paths, which we now define and illustrate.

Definition 1.1. In graph-theoretic language, a 2-tree is defined inductively as follows

- (1) K_3 is a 2-tree.
- (2) If G is a 2-tree, the graph obtained by inserting a vertex adjacent to the two vertices of an edge of G is a 2-tree.

A linear 2-tree (or 2-path) is a 2-tree in which exactly two vertices have degree 2. For an illustration of two sample linear 2-trees see Figure 2.

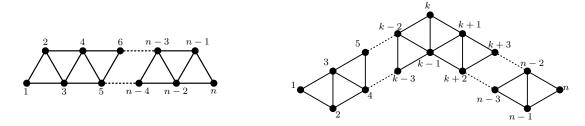


FIGURE 2. On the left, a straight linear 2-tree with n vertices. On the right, a linear 2-tree with n vertices and single bend at vertex k.

In [1] network transformations (identical to those found in Section 3) were used to determine the effective resistance in a linear 2-tree with n vertices; the following results were obtained.

Theorem 1.2. [1, Th. 20] Let S_n be the straight linear 2-tree on n vertices labeled as in the graph on the left in Figure 2. Then for any two vertices u and v of S_n with u < v,

$$r_{S_n}(u,v) = \frac{\sum_{i=1}^{v-u} (F_i F_{i+2u-2} - F_{i-1} F_{i+2u-3}) F_{2n-2i-2u+1}}{F_{2n-2}},$$

or equivalently in closed form

$$r_{S_n}(u,v) = \frac{F_{m+1}^2 + F_{v-u}^2 F_{m-2j-v+u+3}^2}{F_{2m+2}} + \frac{F_{m+1} \left[F_{m-v+u}((v-u)L_k - F_{v-u}) + F_{m-v+u+1} \left((v-u-5)F_{v-u+1} + (2v-2u+2)F_{v-u} \right) \right]}{5F_{2m+2}}$$

where F_p is the pth Fibonacci number and L_q is the qth Lucas number.

Moreover, identity (1.1) was shown.

In [2] the formulas for a straight linear 2-tree were generalized to a linear 2-tree with any number of bends. See the graph on the right in Figure 2 for an example of a linear 2-tree with a bend at vertex k. The following result is the main result from [2] and nicely gives the effective resistance between two vertices in a bent linear 2-tree.

Theorem 1.3. [2, Th. 3.1] Given a bent linear 2-tree with n vertices, and $p = p_1 + p_2 + p_3$ single bends located at nodes k_1, k_2, \ldots, k_p and $k_1 < k_2 < \cdots < k_{p-1} < k_p$ the effective resistance between vertices u and v is given by

$$\begin{split} r_G(u,v) &= r_{S_n}(u,v) \\ &- \sum_{j=p_1+1}^{p_1+p_2} \left[F_{k_j-3} F_{k_j} - 2 \sum_{i=p_1+1}^{j-1} [(-1)^{k_j-k_i+1+j-i} F_{k_i} F_{k_i-3}] + 2 (-1)^{j+u+k_j} F_{u-1}^2 \right] \\ & \cdot \left[F_{n-k_j+2} F_{n-k_j-1} + 2 (-1)^{v-k_j} F_{n-v}^2 \right] / F_{2n-2}. \end{split}$$

In addition, identity (1.2) was shown.

This paper adds to the growing literature on electrical circuits and recursions by presenting, exploring, and proving results about an infinite array, $C_{i,j}$, $j \ge 1, 0 \le i \le 2(j-1)$, whose elements are electrical resistances associated with circuits defined on triangular grid graphs.

2. Some Definitions

This section gathers and defines some assorted terms used throughout the paper.

The (Triangular) n-**grid,** [6, Figure 1], [5, Figure 2]. Figure 3 is illustrative of the general (triangular) n-grid for n = 3. As can be seen the n-grid consists of n rows with $i, 1 \le i \le n$, upright oriented triangles arranged in a triangular grid. Triangles are labeled by row, top to bottom, and diagonal, left to right, as shown in Figure 3.

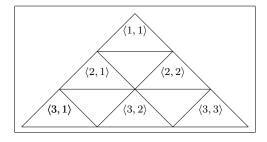


FIGURE 3. A 3-grid with the upright oriented triangles labeled by row and diagonal.

The all-one n-grid. Throughout this paper the edge labels of a graph correspond to actual resistance values. The *all-one* n-grid refers to an n-grid all of whose resistance values are uniformly 1.

We use the notation $T_{r,d,e}$ to refer to the edge label of edge $e, e \in \{L, R, B\}$ (standing, respectively, for the left, right, and base edges of a triangle in the upright oriented position), of the triangle in row r diagonal d. Similarly, $T_{r,d}$ will refer to the triangle in row r diagonal d.

Throughout the paper both the all—one *n*-grid and the *m*-grids $(1 \le m \le n-1)$ derived from it, as described in Section 4, possess vertical and rotational symmetry (when rotated by $\frac{\pi}{3}$), [6, Definition 9.1], [5, Definition 2.11].

This symmetry facilitates not presenting results separately for the left, right, and base sides. Typically we will suffice with presenting results for the the upper left half of a grid, [6, Definition 9.6],[5, Definition 2.12], defined as the set of triangles, $T_{r,d}$ with $0 \le r \le \lfloor \frac{m+1}{2} \rfloor$, $1 \le d \le \lfloor \frac{m+2}{2} \rfloor$.

Example 2.1. If n = 3, (see panel A1 in Figure 5) the upper left half consists of the triangles $\langle r, d \rangle, d = 1, r = 1, 2$.

The importance of the upper left half is the following result which captures the implications of the symmetry of the m-grids [6, Corollary 9.6], [5, Lemma 2.14].

Lemma 2.2. For an m-grid, once the edge values of the upper half are known, all edge values in the m-grid are fixed.

Comment 2.3. A typical use of symmetry and Lemma 2.2 is as follows. We may prove a result for edges under assumptions valid only for the left side of an n-grid in the upper left half. However, by Lemma 2.2 this fixes the edge values in the entire grid. Vertical and rotational symmetry can then identify the labels in any edge of the n-grid using the labels in the upper left half. We will suffice with referring to this comment to indicate that our proof, with assumptions valid in the upper left half, suffices to prove results for the entire n-grid.

Corners. [6, Equation (29)], [5, Definition 2.15]. Graph—theoretically, a triangle is a corner of an m-grid if it has a degree-2 vertex. The 3 corner triangles of an m-grid are located at $T_{1,1}, T_{m,1}, T_{m,m}$. For example, for the 3-grid in Figure 3, the three corners are located at $\langle 1, 1 \rangle, \langle 3, 1 \rangle, \langle 3, 3 \rangle$.

3. The Three Circuit Transformations

As pointed out in Section 1, every circuit has associated with it an underlying labeled graph whose edge labels are electrical resistances. Therefore, to specify an equivalent circuit transformation from an initial parent circuit to a transformed child circuit we must specify the vertex, edge, and label transformations. By equivalent circuit transformation we mean that the effective resistance between specified vertices in the parent and child circuit are equal. There are three basic circuit transformations that we use in this work that preserve effective resistance: series, $\Delta - Y$, and $Y - \Delta$. Figure 4 illustrates the series transformation.

The following are the key points about this transformation.

- The top parent graph has 3 nodes and 2 edges
- The transformed child graph below has 2 nodes and one edge
- There is a formula, [7, pg. 43] $R_1 + R_2$ giving the edge label of the child graph in terms of the edge labels of the parent graph.
- This transformation is *equivalent*; the effective resistance between nodes and A and C are the same in the parent and child graph.

The remaining two circuit transformations are the $\Delta - Y$ transformation which transforms a parent simple 3-edge loop to a claw (3-edge outstar), and the $Y - \Delta$ transformation which

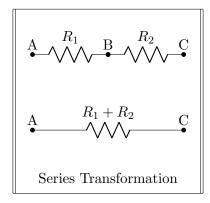


FIGURE 4. Illustration of the series transformations. See narrative for further details.

takes a claw to a 3-edge loop, [6, Figure 2], [5, Definition 2.4]. The relevant transformation functions are

$$\Delta(x,y,z) = \frac{xy}{x+y+z}; \qquad Y(x,y,z) = \frac{xy+yz+zx}{x}. \tag{3.1}$$

Following the computations presented in this paper will not require details of these transformations or how the order of arguments relates to the underlying graphs. To follow the computations needed in this paper it suffices to know the four circuit transformation functions presented in Section 5.

4. The Reduction Algorithm

This section presents the basic reduction algorithm to reduce an n-grid to an (n-1)-grid. This algorithm was first presented in [2, pg. 18] where the algorithm was used for purposes of proof but not used computationally, since computations were done using the combinatorial Laplacian. Hendel [6, Definition 2.3, Figure 3] was the first to use the reduction algorithm computationally. Moreover, [5, Algorithm 2.8, Figure 3] and Section [5, 5] was the first to show that the four transformation functions, presented in Section [5, 5] suffice for all computations. The usefulness of the reduction algorithm in uncovering patterns is alluded to in [5, 6].

We begin the presentation of the four circuit transformations with some basic illustrations. The reduction algorithm takes a parent m grid and reduces it, by removing one row of triangles, to a child (m-1)-grid. Figure 5, illustrates the five steps in reducing the 3-grid (Panel A) to a two grid (Panel E), [6, Steps A-E, Figure 3], [5, Algorithm 2.8].

- Step 1 Panel A: Start with a labeled 3-grid
- Step 2 Panel B: Apply a ΔY transformation to each upright triangle (a 3-loop) resulting in a grid of 3 rows of 3-outstars, as shown.
- Step 3 Panel C: Discard the corner tails, edges with a vertex of degree one. This does not affect the resistance labels of edges in the reduced two grid in panel E. (However, these corner tails are useful for computing certain effective resistances as shown in [2, 4]).
- Step 4 Panel D: Perform series transformations on all consecutive pairs of boundary edges (i.e., the dashed edges in panel C).
- Step 5 Panel E: Apply $Y-\Delta$ transformations to all remaining claws, transforming them into 3-loops.

The important point here is that each of the five steps involves specific circuit transformations. However, to follow, and be able to reproduce the computations in this paper, only the

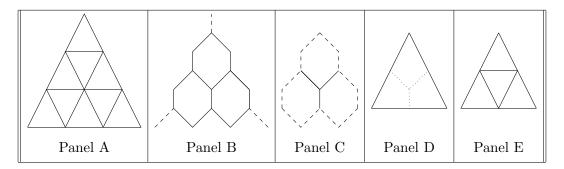


FIGURE 5. Illustration of the reduction algorithm, on a 3-grid. The panel labels correspond to the five steps indicated in the narrative.

four circuit transformation functions presented in the next section are needed. The derivation of these four circuit transformation functions is not needed and has been given in detail in the references cited. Example 4.1 illustrates how knowledge of the four transformation functions suffice to follow the results in this paper.

In the sequel, we will typically start with an all-one n-grid and successively apply the reduction algorithm resulting in a collection of m-grids, $1 \le m \le n-1$. The notation

 $T^m_{r.d.X}, X \in \{L, R, B, LR\}$ indicates the resistance label of side X

in triangle $T_{r,d}$ of the all-one n-grid reduced m times.

The symbol LR will be used in a context

when whether the side is L or R depends on the parity of a parameter.

Alternatively, if we deal with a single reduction we may use the superscripts p, c to distinguish between the parent grid and the child grid if the actual number of reductions used is not relevant.

Example 4.1. Referring to Figure 5, the function $T_{r,d,L}^c$, (5.2), presented in the next section, takes the 9 resistance edge-labels of triangles $T_{2,1}^p$, $T_{2,2}^p$, $T_{3,2}^p$ in the parent 3-grid in Panel A and computes the resistance edge-value, $T_{2,2,L}^c$ of the child 2-grid in Panel E.

This illustrates how the four transformation functions of the next section suffice to verify and reproduce the computations in this paper.

5. The Four Transformation Functions.

The following four circuit transformation functions whose arguments are resistance edge labels in a parent grid suffice to compute any resistance edge label in a child grid.

- Boundary edges
- Base (non-boundary) edges
- Right (non-boundary)edges
- Left (non-boundary) edges

We begin our description of the four transformation functions with the base edge case. We first illustrate with the base edge of the top corner triangle, $T_{1,1}^p$, in Figure 3, Panel E, and then generalize. Note, that the Δ and Y functions have been defined in (3.1).

We have

$$T_{1,1,B}^c = Y(\Delta(T_{3,2,L}^p, T_{3,2,R}^p, T_{3,2,R}^p), \Delta(T_{2,1,R}^p, T_{2,1,R}^p, T_{2,1,L}^p), \Delta(T_{2,2,R}^p, T_{2,2,L}^p, T_{2,2,R}^p)).$$

This is a function of 9 variables. At times it becomes convenient to emphasize the triangles involved. We will use the following notation to indicate the dependency on triangles:

$$T_{1,1,B}^c = F(T_{3,2}^p, T_{2,1}^p, T_{2,2}^p),$$

which is interpreted as saying that the base edge of $T_{1,1}^c$ is some function (F) of the edgelabels of the triangles $T_{3,2}^p, T_{2,1}^p, T_{2,2}^p$. Clearly, this notation is mnemonical and cannot be used computationally. It is, however, very useful in proofs as will be seen later.

The previous two equations can be generalized to an arbitrary m-grid and an arbitrary row and diagonal (with minor constraints, $r+2 \le n, d+1 \le r$, on the row and diagonal). We have

$$\begin{split} T^{c}_{r,d,B} &= Y(\Delta(T^{p}_{r+2,d+1,L}, T^{p}_{r+2,d+1,R}, T^{p}_{r+2,d+1,B}), \Delta(T^{p}_{r+1,d,R}, T^{p}_{r+1,d,B}, T^{p}_{r+1,d,L}), \\ &\qquad \qquad \Delta(T^{p}_{r+1,d+1,B}, T^{p}_{r+1,d+1,L}, T^{p}_{r+1,d+1,R})), \end{split}$$

and

$$T_{r,d,B}^c = F(T_{r+2,d+1}^p, T_{r+1,d}^p, T_{r+1,d+1}^p).$$

We next list the remaining three transformation functions.

For a boundary left edge we have

$$T_{r,1,L}^c = \Delta(T_{r,1,B}^p, T_{r,1,L}^p, T_{r,1,R}^p) + \Delta(T_{r+1,1,L}^p, T_{r+1,1,R}^p, T_{r+1,1,B}^p), \tag{5.1}$$

and

$$T_{r,1,L}^c = F(T_{r,1}^p, T_{r+1,1}^p).$$

For $r+1 \le n, d \ge 2$, for a non-boundary left edge we have,

$$T_{r,d,L}^{c} = Y(\Delta(T_{r,d-1,R}^{p}, T_{r,d-1,B}^{p}, T_{r,d-1,L}^{p}), \Delta(T_{r,d,B}^{p}, T_{r,d,L}^{p}, T_{r,d,R}^{p}), \Delta(T_{r+1,d,L}^{p}, T_{r+1,d,B}^{p}, T_{r+1,d,B}^{p})), (5.2)$$

and

$$T_{r,d,L}^c = F(T_{r,d-1}^p, T_{r,d}^p, T_{r+1,d}^p). (5.3)$$

For $r+1 \le n, d \le r-1$, for a non-boundary right edge we have,

$$\begin{split} T^{c}_{r,d,R} &= Y(\Delta(T^{p}_{r,d+1,B}, T^{p}_{r,d+1,L}, T^{p}_{r,d+1,R}, \Delta(T^{p}_{r+1,d+1,L}, T^{p}_{r+1,d+1,R}, T^{p}_{r+1,d+1,B}), \\ &\qquad \qquad \Delta(T^{p}_{r,d,R}, T^{p}_{r,d,B}, T^{p}_{r,d,L})), \end{split}$$

and

$$T_{r,d,R}^c = F(T_{r,d+1}^p, T_{r+1,d+1}^p, T_{r,d}^p).$$

Comment 5.1. These 4 functions, despite their restrictions, suffice to compute all edge values in the upper left half of the child grid and hence by Comment 2.3 suffice to determine all edge values in the child grid.

6. Computational Examples

The purposes of this section are i) to illustrate computations using the four transformation functions (Section 5) and ii) to provide examples, used in Section 7, to motivate the main theorem.

- 6.1. One Reduction of an all-one n-grid. Recall, an all-one n-grid definitionaly has uniform labels of 1. Hence, using the four circuit transformations introduced in the previous section, we may calculate the edge resistance values in an n-1 grid arising from one reduction of the all-one n grid as follows:
 - By (5.1), $T_{r,1,L} = \Delta(1,1,1) + \Delta(1,1,1) = \frac{2}{3}, 1 \le r \le n-1$.
 - The preceding bullet computes resistance labels for the left boundary. By Comment 2.3 the same computed value holds on the other two boundary edges: $T_{r,r,R} = T_{n-1,r,B} = \frac{2}{3}, 1 \le r \le n-1$.
 - All other edge values are 1, since by the last section, $T_{r,d,X}, X \in \{L, R, B\}$ equals $Y(\Delta(1,1,1), \Delta(1,1,1), \Delta(1,1,1)) = 1$. (Remaining cases not satisfying the restrictions on these four circuit transformation functions are evaluated as 1 using Comment 2.3).

We may summarize our results in a lemma ([6, Corollary 5.1], [5, Lemma 6.1]).

Lemma 6.1. The resistance labels of the n-1 grid arising from one reduction of an all-one n-grid are as follows:

- (1) Boundary resistance labels are uniformly $\frac{2}{3}$.
- (2) Interior resistance labels are uniformly 1.

The labels of the right and left edges of the top corner triangle, $T_{1,1}$, of the n-1 grid is presented in Panel A of Figure 6.

6.2. **Uniform Central Regions.** Prior to continuing with the computations we introduce the concept of *the uniform center* which will be used in the proof of the main theorem.

First, we can identify a triangle with the ordered list, (Left edge, Right edge, Base edge), of its resistance labels. Two triangles are then of the *same type* if their ordered edge labels are equal. By Lemma 6.1 for the once reduced n-1 grid we have

$$T_{r,1} = \left(\frac{2}{3}, 1, 1\right)$$
 and $T_{r,r} = \left(1, \frac{2}{3}, 1\right)$ for $2 \le r \le n - 1$.

Although $T_{r,1}$ and $T_{r,r}$ are not strictly of the same type we will say they are of the same type up to symmetry since each triangle may be derived from the other by a vertical symmetry, [5, Definition 5.8].

Using this concept of type up to symmetry, we note that the central region, $2 \le r \le n-2$, of diagonal 1 of the reduced n-1 grid is uniform, that is, all triangles have the same type up to symmetry. We also note that the interior of the reduced n-1 grid has uniform edge labels.

This presence of uniformity generalizes. The formal statement of the uniform center [5, Theorem 6.2] is as follows:

Theorem 6.2 (Uniform Center). For any $s \ge 1$, let $n \ge 4s$, and $1 \le d \le s$:

(1) *For*

$$s + d \le r \le n - 2s \tag{6.1}$$

the triangles $T_{r,d}^s$ are all of the same type up to symmetry.

(2) *For*

$$2s - 1 \le r \le n - 2s + 1 \tag{6.2}$$

- i) the left sides $T^s_{r,s,L}$ are all identically labeled, ii) $T^s_{2s-1,s,R} = T^s_{2s-1,s,L}$, $T^s_{r,s,R} = 1, 2s \le r \le n-2s$, and iii) $T^s_{r,s,B} = 1, 2s-1 \le r \le n-2s-1$.
- (3) For any triangle in the uniform center, that is, satisfying (6.1), $T_{r,d,R} = T_{r,d,B}$.

Comment 6.3. This theorem has an elegant graphical interpretation [5]. It states that the sub triangular grid whose corner triangles are $T_{2s-1,s}^s, T_{n-2s,s}^s, T_{n-2s,n-2s}^s$ has interior labels of 1 and a single uniform label along its edge boundary. However this interpretation is not needed in the sequel.

6.3. Two Reductions of an all-one n-grid. We continue illustrating computations by considering the n-2 grid arising from 2 reductions of the all-one n-grid (or one reduction of the n-1 grid.)

By (5.3)

$$T_{3,2,L}^2 = F(T_{3,1}^1, T_{3,2}^1, T_{4,2}^1).$$

By Lemma 6.1, we have

$$T_{3,1}^1 = \left(\frac{2}{3}, 1, 1\right), T_{3,2}^1 = T_{4,2}^p = (1, 1, 1).$$

Hence, by (5.2)

$$T_{3,2,L}^c = Y\left(\Delta\left(1,1,\frac{2}{3}\right), \Delta(1,1,1), \Delta(1,1,1)\right) = \frac{26}{27},$$
 (6.3)

as shown in Panel B of Figure 6.

To continue with the computations we define the function,

$$G_0(X) = Y(\Delta(1,1,X), \Delta(1,1,1), \Delta(1,1,1)) = \frac{X+8}{9},$$
 (6.4)

and confirm $G_0(\frac{2}{3}) = \frac{26}{27}$.

6.4. Three Reductions of an all-one *n*-grid. We next compute $T_{5,3,L}^3$. Continuing as in the case of $T_{3,2,1}$, we have by (5.3)

$$T_{5,3,L}^3 = F(T_{5,2}^2, T_{5,3}^2, T_{6,3}^2). (6.5)$$

By Theorem 6.2 Part 2,

$$T_{5,2,L}^2 = T_{3,2,L}^2,$$

and by (6.3)

$$T_{3,2,L}^2 = \frac{26}{27},$$

implying

$$T_{5,2,L}^2 = \frac{26}{27}.$$

Again, by Theorem 6.2 Part 3, all resistance labels of $T_{5,3}^2$, $T_{6,3}^2$ are 1. Plugging this into (6.5) and using (5.2) and (6.4), we have

$$T_{5,3,L}^3 = Y\left(\Delta\left(1,1,\frac{26}{27}\right),\Delta(1,1,1),\Delta(1,1,1)\right) = G_0\left(\frac{26}{27}\right) = \frac{242}{243}.$$

Panel C of Figure 6 illustrates this.

We can continue this process inductively. For example, $T_{7,4}^4 = G_0(\frac{242}{243})$. The result is summarized as follows.

Lemma 6.4. With $G_0(X)$ defined by (6.4) we have $T_{1,1,L}^1 = \frac{2}{3}$ and for $s \geq 2$, $T_{2s-1,s,L} = G_0(T_{2s-3,s-1,L})$.

An almost identical argument using the circuit transformations for the right edge shows the following.

Lemma 6.5. Let
$$G_1(X) = \frac{1}{3} \frac{X+8}{X+2}$$
. Then for $k \ge 0$, $T_{3+2k,1+k,R}^{2+k} = G_1(T_{1+2k,1+k,L}^{1+k})$.

7. MOTIVATION FOR THE CIRCUIT ARRAY

This section motivates the underlying construction of the Circuit Array. We start with an all—one n-grid. Throughout, we assume n sufficiently large so that the hypotheses of Theorem 6.2 holds. As computed in Section 6, we have:

- $T^1_{1,1,L} = \frac{2}{3} = 1 \frac{3}{9^1}$; see Panel A of Figure 6. $T^2_{3,2,L} = \frac{26}{27} = 1 \frac{3}{9^2}$; see Panel B of Figure 6. $T^3_{5,3,L} = \frac{242}{243} = 1 \frac{3}{9^3}$; see Panel C of Figure 6.

The resulting sequence

$$\frac{2}{3}, \frac{26}{27}, \frac{242}{243}, \dots$$

satisfies

$$1 - \frac{3}{0^s}, s = 1, 2, 3, \dots$$
 (7.1)

In this particular case the denominators, 9^s form a linear homogeneous recursion with constant coefficients (LHRCC) of order 1,

$$G_s = 9G_{s-1}, s > 2, \qquad G_1 = 9.$$

Similarly, the numerators satisfy the LRCC,

$$G_s = 9G_{s-1} + 24, s \ge 2,$$
 $G_1 = 6$

(and therefore, since a sequence satisfying a linear, non-homogeneous, recursion, with constant coefficients (LRCC) will also satisfy an LHRCC albeit with a higher degree), the sequence also satisfies an LHRCC.

The sequence just studied forms row 0 of the Circuit Array, Table 2. To determine row 1 of the Circuit Array we compute the following:

- The right side of the triangle left-adjacent to the top corner triangle of the 2-rim of reduction 2 has label $\frac{13}{12} = 1 + \frac{2}{3} \frac{1}{9^{2-1}-1}$, as shown in Panel B of Figure 6.
- The right side of the triangle left-adjacent to the top corner triangle of the 3-rim of reduction 3 has label $\frac{121}{120} = 1 + \frac{2}{3} \frac{1}{9^{3-1}-1}$, as shown in Panel C of Figure 6.
- The right side of the triangle left-adjacent to the top corner triangle of the 4-rim of reduction 4 has label $\frac{1093}{1092} = 1 + \frac{2}{3} \frac{1}{9^{4-1}-1}$.

The resulting sequence

$$\frac{13}{12}, \frac{121}{120}, \frac{1093}{1092}, \frac{9841}{9840}, \dots$$

satisfies $1 + \frac{2}{3} \frac{1}{0^{s-1}-1}$. We again see the presence of LRCC. The sequence of twice the denominators satisfies the LRCC

$$G_{s+1} = 9G_s + 24, \ s > 3, \ G_2 = 24,$$

while the sequence of twice the numerators satisfies the LRCC,

$$G_{s+1} = 9G_s + 8, \ s > 3, \ G_2 = 26.$$

and hence both numerators and denominators satisfy LHRCC, albeit of higher order.

These calculations determine the construction of the Circuit Array by rows. Figure 6 can be used to motivate a construction by columns. Each perspective provides different sequences.

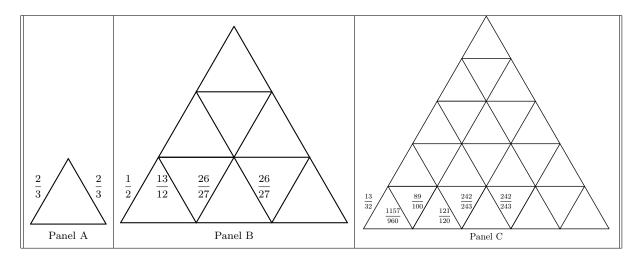


FIGURE 6. Graphical illustration showing locations of various edge resistance values computated in this section. See the narrative for further details.

- As shown in Panel A, Column 1, consists of the singleton $\frac{2}{3}$. We may describe the process of generating this column by i) starting at the left resistance edge label of triangle $T_{1,1}$, where 1 corresponds to the number of underlying reductions of the allone n-grid, ii) traversing to the left (in this case there is nothing further to transverse), and iii) ending at the left–most edge of the underlying row. This singleton $\frac{2}{3}$ is column 1 of the Circuit Array, Table 2.
- As shown in Panel B, Column 2 may be obtained as follows: i) Start at the left resistance-label of triangle $T_{3,2}$, where the number of reductions of the all—one n-grid for this column is 2, and $3 = 2 \times 2 1$. This resistance is $\frac{26}{27}$. Then ii) traverse to the left, and iii) end at the left-most edge of the underlying row. By recording the labels during this transversal we obtain $\frac{26}{27}, \frac{13}{12}, \frac{1}{2}$, which is column 2 of the Circuit Array, Table 2, starting at row 0 and ending at row 2.
- As shown in Panel C, Column 3 may be obtained as follows: i) Start at the left resistance-label of triangle $T_{5,3}$, where the number of reductions of the all—one n-grid for this column is 3, and $5 = 2 \times 3 1$. This resistance is $\frac{242}{243}$. Then ii) traverse to the left, and iii) end at the left most edge of the underlying row. By recording the labels during this transversal we obtain $\frac{242}{243}, \frac{121}{120}, \frac{89}{100}, \frac{1157}{960}, \frac{13}{32}$, which is column 3 of the Circuit Array, Table 2, starting at row 0 and ending at row 4.
- The above suggests in general, that column $c \ge 1$ of the Circuit Array will consist of the resistance labels of the left and right sides of the triangles $T_{2c-1,i}^c$, $i = c, c-1, \ldots, 1$. This will be formalized in the next section.

8. The Circuit Array

We formalize the Circuit Array, by defining the entry in row $i, 0 \le i \le 2(j-1)$, and column $j, j \ge 1$, as equal to $T^j_{2j-1,j-\lfloor \frac{i+1}{2}\rfloor,LR}$ where, as indicated at the end of Section 4 the symbol LR means L (respectively R) if i is even (respectively odd).

Example 8.1. This example repeats the derivation of the first three columns derived at the end of 7.

Table 1. First few rows and columns of the formal entries of the Circuit Array.

	1	2	3	4	5	6	7	
0	$T^1_{1,1,L}$	$T_{3,2,L}^2$	$T_{5,3,L}^3$	$T_{7,4,L}^4$	$T_{9,5,L}^5$	$T_{11,6,L}^{6}$	$T_{13,7,L}^{7}$	
1		$T_{3,1,R}^2$	$T_{5,2,R}^3$	$T_{7,3,R}^4$		$T_{11,5,R}^6$	$T^7_{13,6,R}$	
2		$T_{3,1,L}^2$	$T_{5,2,L}^{3}$	$T_{7,3,L}^4$	$T_{9,4,L}^5$	$T_{11,5,L}^6$	$T^7_{13,6,L}$	
3			$T_{5,1,R}^3$	$T_{7,2,R}^4$	$T_{9,3,R}^5$	$T_{11,4,R}^6$	$T^7_{13,5,R}$	
4			$T_{5,1,L}^3$	$T_{7,2,L}^4$	$T_{9,3,L}^5$	$T_{11,4,L}^{6}$	$T^7_{13,5,L}$	
5				$T_{7,1,R}^4$	$T_{9,2,R}^5$	$T_{11,3,R}^6$	$T^7_{13,4,R}$	
6				$T_{7,1,L}^4$	$T_{9,2,L}^5$	$T_{11,3,L}^6$	$T^7_{13,4,L}$	
7					$T_{9,1,R}^5$	$T_{11,2,R}^6$	$T^7_{13,3,R}$	
8					$T_{9,1,L}^5$	$T_{11,2,L}^6$	$T^7_{13,3,L}$	
9						$T_{11,1,R}^6$	$T_{13,2,R}^{7}$	
10						$T_{11,1,L}^{6}$	$T^7_{13,2,L}$	
11							$T^7_{13,1,R}$	
12							$T^7_{13,1,L}$	
13								$ \cdot \cdot $
14								٠٠.

Referring to Figure 6, we see Panel A contains row 0, column 1 of the array containing $T^{j}_{2j-1,j-i} = T^{1}_{1,1} = \frac{2}{3}$.

Panel B contains column 2, rows 0,1,2, which respectively contain $T_{3,2,L}^2 = \frac{26}{27}, T_{3,1,R}^2 = \frac{13}{12}, T_{3,1,L}^2 = \frac{1}{2}$.

Panel C contains column 3, rows 0,1,2,3,4 which respectively contain $T_{5,3,L}^3 = \frac{242}{243}, T_{5,2,R}^3 = \frac{121}{120}, T_{5,2,L}^3 = \frac{89}{100}, T_{5,1,R}^3 = \frac{1157}{969}, T_{5,1,L}^3 = \frac{13}{32}$.

Table 1 presents the first few rows and columns of formal entries of the Circuit Array while Table 2 presents the first few rows and columns of the numerical values of the Circuit Array.

The *leftmost* diagonal of the circuit array is defined by

$$L_s = T_{2s-1,1,L}^s, s = 1, 2, 3, \dots$$
 (8.1)

9. The Main Theorem

The main theorem asserts that the Circuit Array is a recursive array. Along any fixed row, table values are a uniform function of previous row and column values.

Theorem 9.1. For each $e \ge 0$, e even, there exist rational functions G_e such that for $k \ge 0$

$$T_{e+3+2k,2+k,L}^{\frac{e}{2}+2+k} = G_e(T_{1+2k,1+k,L}^{1+k}, T_{3+2k,1+k,L}^{2+k}, \dots, T_{e+1+2k,1+k,L}^{\frac{e}{2}+1+k}). \tag{9.1}$$

Similarly, for each positive odd, o = e + 1,

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$$T_{o+2+2k,1+k,R}^{\frac{o-1}{2}+2+k} = G_o(T_{1+2k,1+k,L}^{1+k}, T_{3+2k,1+k,L}^{2+k}, \dots, T_{e+1+2k,1+k,L}^{\frac{e}{2}+1+k}). \tag{9.2}$$

							1
	1	2	3	4	5	6	
0	$\frac{2}{3}$	$\frac{26}{27}$	$\frac{242}{243}$	$\frac{2186}{2187}$	$\frac{19682}{19683}$	$\frac{177146}{177147}$	
1		$\frac{13}{12}$	$\frac{121}{120}$	$\frac{1093}{1092}$	$\frac{9841}{9840}$	$\frac{88573}{88572}$	
2		$\frac{1}{2}$	$\frac{89}{100}$	$\frac{16243}{16562}$	$\frac{335209}{336200}$	$\frac{108912805}{108958322}$	
3			$\frac{1157}{960}$	$\frac{1965403}{1904448}$	$\frac{366383437}{364552320}$	$\frac{1071810914005}{1071023961216}$	
4			$\frac{13}{32}$	$\frac{305041}{380192}$	$\frac{1303624379}{1372554304}$	$\frac{9044690242835}{9138722473024}$	
5				$\frac{224369}{167424}$	$\frac{19373074829}{18067568640}$	$\frac{308084703953915}{303469074613248}$	
6				$\frac{89}{256}$	$\frac{296645909}{412902400}$	$\frac{31631261501245}{34990560891392}$	
7					$\frac{46041023}{31211520}$	$\frac{112546800611915}{99980909002752}$	
8					$\frac{2521}{8192}$	$\frac{320676092095}{495976128512}$	
9						$\frac{4910281495}{3059613696}$	
10						$\frac{18263}{65536}$	

TABLE 2. First few rows and columns of the numerical values of the Circuit Array.

Example 9.2. Lemma 6.4 and Lemma 6.5 introduced the row 0 function, G_0 , (6.4), and row 1 function, $G_1(X)$, respectively.

Proof of the main theorem is deferred to Sections 15 and 16. Illustrative examples of these recursions are provided in the next section. Note, that all functions in the next section are derived by plugging into the four circuit transformations similar to what we did for Lemma 6.4 and Lemma 6.5.

10. Illustrations of the Main Theorem

Example 10.1. For i = 0 we have by Lemma 6.4,

$$G_0(X) = \frac{X+8}{9}.$$

Hence,

$$C_{0,2} = \frac{26}{27} = G_0(C_{0,1}) = G_0\left(\frac{2}{3}\right), \text{ and } C_{0,3} = \frac{242}{243} = G_0(C_{0,2}) = G_0\left(\frac{26}{27}\right).$$

Similarly, we have by Lemma 6.5,

$$G_1(X) = \frac{1}{3} \frac{X+8}{X+2}.$$

Hence,

$$C_{1,2} = \frac{13}{12} = G_1(C_{0,1}) = G_1\left(\frac{2}{3}\right), \text{ and } C_{1,3} = \frac{121}{120} = G_1(C_{0,2}) = G_1\left(\frac{26}{27}\right).$$

Example 10.2. For i = 1 we have

$$G_2(X,Y) = \frac{9Y(X+2)^2 + 8(X+8)^2}{(X+26)^2}.$$

Hence,

$$C_{2,3} = \frac{89}{100} = G_2(C_{0,1}, C_{2,2}) = G_2\left(\frac{2}{3}, \frac{1}{2}\right), \text{ and}$$

$$C_{2,4} = \frac{16243}{16562} = G_2(C_{0,2}, C_{2,3}) = G_2\left(\frac{26}{27}, \frac{89}{100}\right).$$

Similarly, we have

$$G_3(X,Y) = \frac{9Y(X+2)^2(X+8) + 8(X+8)^3}{9Y(X+2)^2(X+26) + 6(X+2)(X+8)(X+26)}.$$

Hence,

$$C_{3,3} = \frac{1157}{960} = G_3(C_{0,1}, C_{2,2}) = G_3\left(\frac{2}{3}, \frac{1}{2}\right), \text{ and}$$

$$C_{3,4} = \frac{1965403}{190448} = G_3(C_{0,2}, C_{2,3}) = G_3\left(\frac{26}{27}, \frac{89}{100}\right).$$

Example 10.3. For i = 2, we have $G_4(X, Y, Z) = \frac{N(X, Y, Z)}{D(X, Y, Z)}$, with

$$N(X,Y,Z) = \begin{cases} 512(X+2)^{0}(X+8)^{5}(X+80)Y^{0} + \\ 1152(X+2)^{2}(X+8)^{3}(X+80)Y^{1} + \\ 648(X+2)^{4}(X+8)^{1}(X+80)Y^{2} + \\ 36(X+2)^{2}(X+8)^{2}(X+80)^{2}Y^{0}Z + \\ 108(X+2)^{3}(X+8)^{1}(X+80)^{2}Y^{1}Z + \\ 81(X+2)^{4}(X+8)^{0}(X+80)^{2}Y^{2}Z, \end{cases}$$

and

$$D(X,Y,Z) = \begin{cases} 676(X+2)^{0}(X+8)^{2}Q(X)^{2}Y^{0} + \\ 1404(X+2)^{2}(X+8)^{2}Q(X)^{1}Y^{1} + \\ 729(X+2)^{4}(X+8)^{2}Q(X)^{0}Y^{2}, \end{cases}$$

with, $Q(X) = 13X^2 + 298X + 2848$.

These polynomials are formatted to show certain underlying patterns the statement and proof of which will be the subject of another paper.

of which will be the subject of another paper. One then has
$$C_{4,4} = \frac{305041}{380192} = G_4(C_{0,1}, C_{2,2}, C_{4,3}) = G_4\left(\frac{2}{3}, \frac{1}{2}, \frac{13}{32}\right)$$
.

11. ALTERNATE APPROACHES TO THE MAIN THEOREM

The main theorem formulates the recursiveness of the Circuit Array in terms of recursions by rows with the number of arguments of these recursions growing by row. There are other approaches to formulating the main theorem, explored in the next few sections.

- Section 12 explores a formulation of the main theorem in terms of closed formula similar to those found in Section 7.
- Section 12 also explores formulation of the main theorem in terms of a single variable rather than multiple variables.
- Section 13 explores determining an LHRCC for the numerators of the leftmost diagonal and strongly conjectures its impossibility. This contrasts with other 2-dimensional arrays whose diagonals do satisfy LHRCC.
- Section 14 explores asymptotic approximations to the leftmost diagonal.

12. Recursions vs. Closed Formulae

This section explores a closed-formula approach to the main theorem. We begin with a review.

We have already seen (Lemmas 6.4 and 6.5) that row 0 of the circuit array has a simple closed form,

$$C_{0,s} = 1 - \frac{3}{9^s} \qquad s \ge 1;$$

and similarly, row 1 also has a simple closed form,

$$C_{1,s} = 1 + \frac{2}{3} \frac{1}{9^{s-1} - 1}.$$

This naturally motivated seeking a formulation of the entire array in terms of closed formulae. However, this approach quickly becomes excessively cumbersome. For example, consider row 2. With the aid of [9, A163102, A191008], we found the following closed form for this row:

Let
$$n = 2(s-2)$$
, $d = \frac{1}{2} \left(3^{s-1} - 1 \right)$,
$$N = \frac{1}{4} \left(n \cdot 3^{n+1} \right) + \frac{1}{16} \left(5 \cdot 3^{n+1} + (-1)^n \right), \qquad D = \frac{1}{2} d^2 (d+1)^2.$$

Then

$$C_{2,s} = 1 - \frac{N}{D}.$$

However, this formula is much more complicated than the formula presented in Section 10,

$$C_{2,s} = G_2(X,Y) = \frac{9Y(X+2)^2 + 8(X+8)^2}{(X+26)^2},$$
 with $X = C_{0,s-2}, Y = C_{2,s-1}$.

We present one more attempt at a closed formula which also failed, that of using a single variable. We begin by first re-labeling $\frac{2}{3}$ as $1-\frac{3}{x}$ in the first reduction of an all—one *n*-grid (see Panel A in Figure 6). If we then continue reductions, all labels are rational functions in this single variable x so that upon substitution of x=9 we may then obtain desired resistance edge labels.

As before, the resulting formulas are highly complex. We present below these closed formulas for the left-side diagonal, $\{L_s\}_{s\geq 1}$, (8.1). These closed formula are derived by "plugging in" to the four basic transformation functions of Section 5 as illustrated in Section 7.

$$\frac{x-3}{r} \qquad \text{gives } L_1 = \frac{2}{3} \text{ when } x = 9$$

 $\frac{2}{3}\frac{x-3}{x-1} \qquad \text{gives } L_2 = \frac{1}{2} \text{ when } x = 9$

$$\frac{(x-3)(3x-1)}{6(x-1)^2}$$
 gives $L_3 = \frac{13}{32}$ when $x = 9$

$$\frac{(x-3)(3(x-1)(x-3)+4(3x-1)^2)}{96(x-1)^3}$$
 gives $L_4 = \frac{89}{256}$ when $x = 9$

$$\frac{(x-3)(3(x-1)(x-3)(34x-18)+16(3x-1)^3)}{1536(x-1)^4}$$
 gives L_5 when $x=9$

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$$\frac{(x-3)(3(x-1)(x-3)(793x^2-874x+273)+64(3x-1)^4)}{24576(x-1)^5}$$
 gives L_6 when $x=9$

$$\frac{(x-3)(6(x-1)(x-3)(7895x^3 - 13549x^2 + 8693x - 2015) + 4^4(3x-1)^5)}{393216(x-1)^6}$$

gives L_7 when x=9.

There are interesting patterns in the above results and it may yield future results. One example of an interesting pattern is found in the constants appearing in the denominators. For $s \geq 3$ the denominator constants in the formulas yielding L_s upon substitution of x = 9, satisfy $3 \times 2^{4(s-3)+1}$ We however do not further pursue this in this paper.

Summary: Because of the greater complexity as well as lack of completely describable patterns in the closed formula we abandoned this approach to formulate the main theorem in favor of a recursive approach in several variables as done in Section 9.

13. Impossibility of a recursive sequence for the left-most diagonal

It is natural, when studying sequences of fractions, to separately study their numerators and denominators. We have seen that for C_0, C_1 such an approach uncovers LHRCC. Therefore, it comes as a surprise to have a result stating the impossibility of an LHRCC.

To present this impossibility result, we first, briefly review a technique for discovering LHRCC. Suppose we have an integer sequence such as G_1, G_2, \ldots . Suppose further we believe this sequence is second order, that is,

$$G_n = xG_{n-1} + yG_{n-2}.$$

As n varies this last equation generates an infinite number of equations in x and y. In other words, to investigate the possible recursiveness of this sequence we can solve the following set of equations for any m and the use the solution to test further,

$$\begin{bmatrix} G_m & G_{m+1} \\ G_{m+1} & G_{m+2} \end{bmatrix} \begin{bmatrix} x \\ y \end{bmatrix} = \begin{bmatrix} G_{m+2} & G_{m+3} \end{bmatrix}.$$

Solving this set of equations by Cramer's rule naturally motivates considering the determinant

$$\begin{vmatrix} G_m & G_{m+1} \\ G_{m+1} & G_{m+2} \end{vmatrix}$$

for any integer m. While these determinants are non-zero, the order 3 determinants,

$$\begin{vmatrix} G_m & G_{m+1} & G_{m+2} \\ G_{m+1} & G_{m+2} & G_{m+3} \\ G_{m+2} & G_{m+3} & G_{m+4} \end{vmatrix},$$

must be zero because of the dependency captured by the LHRCC.

These remarks generalize to r-th order recursions for integer $r \geq 2$, and explain why in the search for recursions it is natural to consider such determinants. It follows that if for some m and for all $r \geq 2$ the following determinant is non-zero,

$$\begin{vmatrix} G_m & G_{m+1} & \dots & G_{m+r} \\ G_{m+1} & G_{m+2} & \dots & G_{m+r+1} \\ \dots & \dots & \dots & \dots \\ G_{m+r-1} & G_{m+r} & \dots & G_{m+2r-1} \end{vmatrix},$$

then it is impossible for the sequence $\{G_m\}$ to satisfy any LHRCC.

The following conjecture, verified for several dozen early values of j shows a remarkable and unexpected simplicity in the values of these determinants.

Conjecture 13.1. Let $T(j) = \frac{j(j+1)}{2}$ indicate the j-th triangular number. Using (8.1), define n_s, d_s and n'_s by $L_s = \frac{n_s}{d_s} = \frac{n'_s}{2^{4s-7}}$, where n_s and d_s are relatively prime. For any $j \geq 2$ we have

$$\begin{vmatrix} n'_2 & n'_3 & \cdots & n'_{2+j} \\ n'_3 & n'_4 & \cdots & n_{3+j} \\ \vdots & \vdots & \ddots & \vdots \\ n'_{2+j} & n'_{3+j} & \cdots & n'_{2+2j} \end{vmatrix} = 9^{T(j-1)}.$$

Corollary 13.2. Under the conditions stated in the conjectures, it is impossible for the $\{n'_s\}_{s>1}$ to satisfy an LHRCC of any order.

Comment 13.3. It is tempting to suggest that the numerators satisfy no LHRCC because they are growing too fast. But that is not true. We know that $L_s < 1$, [5, Corollary 7.2] and that the denominators form a geometric sequence. It follows that the numerators are bounded by a geometric sequence. In terms of growth rate, there is no reason why the sequence shouldn't be able to satisfy an LHRCC.

14. An Asymptotic Approach

Prior to presenting the proof of the main theorem, we explore one more approach in this section. By way of motivation recall that several infinite arrays have asymptotic formulas associated with them. For example, the central binomial coefficients have asymptotic formulas arising from Stirling's formula.

For purposes of expositional smoothness, we focus on the leftmost diagonal, L_s , (8.1).

Hendel [6] introduced the idea of finding explicit formulas for edge-values in terms of products of factors. After numerical experimentation, the following approximation was found,

$$L_s \simeq A_s = \frac{2}{3} \prod_{i=2}^{s} (1 - \frac{1}{2i-1}),$$
 (14.1)

with A standing for approximation. Tables 3 and 4 provide numerical evidence for this approximation. The key takeaways from both tables is that both differences $L_s - A_s$ and ratios $\frac{L_s}{A_s}$ are monotone decreasing for $s \geq 3$.

TABLE 3. Numerical evidence for conjectures about L_s , first five rows. Notice that after s = 2 all difference and ratio columns are monotone decreasing.

s	L_s	A_s	$L_s - A_s$	$\frac{L_s}{A_s}$	P_s	$A_s - P_s$	$\frac{A_s}{P_s}$	$L_s - P_s$	$\frac{L_s}{P_s}$
1	0.6667	0.6667	0	1	0.5908	0.0758	1.1284	0.0758	1.1284
2	0.5	0.4444	0.0556	1.125	0.4178	0.0267	1.0638	0.0822	1.1968
3	0.4063	0.3556	0.0507	1.1426	0.3411	0.0144	1.0424	0.0651	1.191
$\parallel 4 \mid$	0.3477	0.3048	0.0429	1.1407	0.2954	0.0094	1.0317	0.0522	1.1769
5	0.3077	0.2709	0.0368	1.136	0.2642	0.0067	1.0253	0.0435	1.1647

Table 4. Numerical evidence for conjectures about L_s , first 80 rows. Observe that except for a few initial values the difference and ratio columns are monotone decreasing.

s	L_s	A_s	$L_s - A_s$	L_s/A_s	P_s	$A_s - P_s$	A_s/P_s	$L_s - P_s$	L_s/P_s
8	0.2387	0.2122	0.0265	1.125	0.2089	0.0033	1.0157	0.0298	1.1427
16	0.1658	0.1489	0.017	1.1141	0.1477	0.0012	1.0078	0.0181	1.1228
24	0.1346	0.1212	0.0134	1.1103	0.1206	0.0006	1.0052	0.014	1.1161
32	0.1162	0.1049	0.0114	1.1084	0.1044	0.0004	1.0039	0.0118	1.1127
40	0.1038	0.0937	0.0101	1.1072	0.0934	0.0003	1.0031	0.0103	1.1107
48	0.0946	0.0855	0.0091	1.1065	0.0853	0.0002	1.0026	0.0093	1.1094
56	0.0875	0.0791	0.0084	1.1059	0.079	0.0002	1.0022	0.0086	1.1084
64	0.0818	0.074	0.0078	1.1055	0.0739	0.0001	1.002	0.008	1.1077
72	0.0771	0.0697	0.0073	1.1052	0.0696	0.0001	1.0017	0.0075	1.1071
80	0.0731	0.0662	0.0069	1.105	0.0661	0.0001	1.0016	0.007	1.1067

The P columns in these tables (which also provide good approximations as measured by differences and ratios) correspond to the following further approximation

$$A_s \approx P_s = \sqrt{\frac{\pi}{9s}},\tag{14.2}$$

with P standing for the approximation of A_s with π .

Equation (14.2) is naturally derived from (14.1) using Stirling's formula. The next lemma contains a formal statement of the result.

Lemma 14.1.

$$A_s \simeq P_s$$
.

Proof. By (14.1) we have

$$A_s = \frac{2}{3} \left(\frac{2}{3} \frac{4}{5} \dots \frac{2s-2}{2s-1} \right).$$

Applying the identity $(2s-1)! = (2 \cdot 4 \cdots (2s-2))(3 \cdot 5 \cdots (2s-1))$ to the last equation, we have

$$A_s = \frac{2}{3} \frac{\left(2^{s-1}(s-1)!\right)^2}{(2s-1)!}.$$

Of the many forms of Stirling's formula, we can simplify the last equation by applying the standard approximation (see for example [8]) $n! \simeq \left(\frac{n}{e}\right)^n \sqrt{2\pi n}$, yielding

$$A_s = \frac{2}{3} \frac{4^s}{4} \left(\frac{s-1}{e} \right)^{2(s-1)} \left(2\pi(s-1) \right) \left(\frac{e}{2s-1} \right)^{2s-1} \frac{1}{\sqrt{2\pi(2s-1)}}.$$

By gathering constants, cancelling the powers of e, and using the fact that $c_1s - c_2 \approx s$ for constants c_1, c_2 , we can simplify this last equation to

$$A_s = \frac{e}{6} 4^s \sqrt{s} \sqrt{\pi} (s-1)^{2s-2} \left(\frac{1}{2s-1} \right)^{2s-1}.$$

Further simplification is obtained by using traditional calculus identities on limits resulting in powers of e.

$$(s-1)^{2s-2} = \left(\frac{s-1}{s}\right)^{2s} s^{2s} \frac{1}{(s-1)^2} \approx e^{-2} s^{2s} \frac{1}{s^2},$$
$$\frac{1}{(2s-1)^{2s-1}} = \left(\frac{2s}{2s-1}\right)^{2s-1} \frac{1}{(2s)^{2s-1}} \approx e^{\frac{1}{4s}} \frac{1}{s^{2s}} 2s.$$

Combining these last 3 equations, cancelling powers of e and 4, and using the fact that $c_1s+c_2 \approx$ s, we obtain

$$A_s \simeq \sqrt{\pi} \frac{1}{6} 2s \frac{1}{s^2} \sqrt{s} = \frac{\sqrt{\pi}}{3\sqrt{s}} = \sqrt{\frac{\pi}{9s}} = P_s$$

as required.

15. Base Case of the Inductive Proof

The proof of the main theorem is by induction on the row index, parameterized by whether the row is even or odd, as shown in equations (9.1)–(9.2). The base case requires proofs for rows 0, 1, 2, 3.

We suffice throughout the proof with consideration of the the even rows, the proof for the odd rows being highly similar and hence omitted. A proof for rows zero and one is presented in Lemmas 6.4 and 6.5. Proofs in this and the next section are accomplished similarly by applying the appropriate transformation functions found in Section 5 coupled with application of the Uniform Center Theorem, Theorem 6.2.

In this section we prove (9.1) for the case e=2. We accomplish this by first proving (9.1) when k=0 and then, using an induction argument, proving for k>0.

Case 1: Proof of Equation (9.1) for e = 2, k = 0.

We must show

$$T_{5,2,L}^3 = G_3(T_{1,1,L}^1, T_{3,1,L}^2), (15.1)$$

for some function G_3 .

By the formula for non-boundary left edges, (5.3), we know

$$T_{5,2,L}^3 = F(T_{5,1}^2, T_{5,2}^2, T_{6,2}^2). (15.2)$$

We further simplify this as follows:

- By Theorem 6.2 Part (2) the six edges of triangles $T_{5,2}^2, T_{6,2}^2$ are identically one.
- By (6.1), the uniform center for the first diagonal, d=1 in the n-grid reduced s=2times contains all triangles on rows, r satisfying $r \geq s + d = 2 + 1 = 3$. Therefore, by Theorem 6.2 Part (1), the argument $T_{5,1}^2$ in (15.2) may be replaced by the identically labeled triangle $T_{3,1}^2$.
- Triangle $T_{3,1}^2$ has three sides, $T_{3,1,L}^2, T_{3,2,R}^2, T_{3,2,B}^2$. But, by Lemma 6.5, $T_{3,2,R}^2 = G_0(T_{1,1,L}^1)$, and by Theorem 6.2 Part (3), $T_{3,2,B}^2 = T_{3,2,R}^2$.

Applying the above to (15.2) and plugging into (5.2) we have

$$T_{5,2,L}^3 = Y(\Delta(T_{3,1,L}^2, G_0(T_{1,1,L}^1), G_0(T_{1,1,L}^1)), \Delta(1,1,1), \Delta(1,1,1)) = G_3(T_{1,1,L}^1, T_{3,1,L}^2),$$

which has the required form of (15.1) as desired. This completes the proof of (9.1) for the case e = 2, k = 0.

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Case 2: Proof of Equation (9.1) for e = 2, k > 0. The proof is by induction. The base case k = 0 was just shown. For an induction assumption we assume (9.1) holds for all k < Kand proceed to prove (9.1) for the case K.

Proceeding exactly as we did in the case k = 0, we have by (5.3),

$$T_{5+2K,2+K,L}^{3+K} = F(T_{5+2K,1+K}^{2+K}, T_{5+2K,2+K}^{2+K}, T_{6+2K,2+K}^{2+K}). \tag{15.3}$$

Continuing as in the case k = 0 we have

- By Theorem 6.2 Part (2) the six edges of triangles $T_{5+2K,2+K}^{2+K}, T_{6+2K,2+K}^{2+K}$ are identically
- By (6.1), the uniform center for the diagonal d = (1 + K) of the all-one n grid reduced s=2+K times contains all triangles on rows r satisfying $r \geq s+d=2+K+1+K=$ 3+2K. Therefore, by Theorem 6.2 Part (1) the argument $T_{5+2K,1+K}^{2+K}$ in (15.3) may be
- replaced by the identically labeled triangle $T_{3+2K,1+K}^{2+K}$.

 Triangle $T_{3+2K,1+K}^{2+K}$ has three sides, $T_{3+2K,1+K,L}^{2+K}$, $T_{3+2K,1+K,R}^{2+K}$, $T_{3+2K,2+K,B}^{2+K}$.

 But by Lemma 6.5, $T_{3+2K,2+K,R}^{2+K} = G_0(T_{1+2K,1+K,L}^{1+K})$, and by Theorem 6.2 Part (3), $T_{3+2K,2+K,B}^{2+K} = T_{3+2K,2+K,R}^{2+K}$.

Applying the above to (15.3) and plugging into (5.2), we have

$$T_{5+2K,2+K,L}^{3+K} = Y(\Delta(T_{3+2K,1+K,L}^{2+K}, G_0(T_{1+2K,1+K,L}^{1+K}), G_0(T_{1+2K,1+K,L}^{1+K})), \Delta(1,1,1), \Delta(1,1,1))$$

$$= G_3(T_{1+2K,1+K,L}^{1+K}, T_{3+2K,1+K,L}^{2+K}),$$

which has the required form of (15.1) as was to be shown. This completes the proof of (9.1) for the second case and hence completes the proof of the base case e=2.

16. Proof of the Main Theorem

This section completes the inductive proof of the main theorem, by proving equation (9.1) for $e \geq 4$. For an induction assumption we assume (9.1) holds for all e < E and proceed to prove (9.1) for the case E. Since we have proven (9.1) for e = 0, 2 in the previous section we may assume

$$E \ge 4. \tag{16.1}$$

We will utilize the following lemma, whose proof follows from an inspection of Table 1.

Lemma 16.1. Triangle $T_{b,c,LR}^a$ belongs to row d column e of the Circuit array if b = 2a - 1, a = e and either i) LR = L, a = c, d = 0, ii) LR = L, d = 2(a - c), or iii) LR = R, d = 02(a-c)-1>0.

First, by (5.3) we have

$$T_{E+3+2k,2+k,L}^{\frac{E}{2}+2+k} = F(T_{E+3+2k,1+k}^{\frac{E}{2}+1+k}, T_{E+3+2k,2+k}^{\frac{E}{2}+1+k}, T_{E+4+2k,2+k}^{\frac{E}{2}+1+k}).$$
(16.2)

Second, in the three triangle arguments on the right hand side of (16.2), we assert that E+3+2k and E+4+2k can be replaced with E+1+2k. To see this note that by Theorem 6.2 Part (1), the uniform center for diagonal d=2+k and for reduction $s=\frac{E}{2}+1+k$, includes triangles on all rows r satisfying, $r \ge d+s$. Since by (16.1), $E+1+2k \ge d+s = 2+k+\frac{E}{2}+k+1$ the assertion is verified. A similar argument holds for the uniform center for diagonal d = 1 + kand for reduction $s = \frac{E}{2} + 1 + k$, and is omitted. These assertions imply

$$T_{E+3+2k,2+k,L}^{\frac{E}{2}+2+k} = F(T_{E+1+2k,1+k}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k}^{\frac{E}{2}+1+k}).$$
 (16.3)

Third, expanding (5.3) to a full 9 variable function, (5.2), and using Theorem 6.2 Part (3) we have that (16.3) is expanded to

$$\begin{split} T_{E+3+2k,1+k,L}^{\frac{E}{2}+2+k} &= Y(\Delta(T_{E+1+2k,1+k,L}^{\frac{E}{2}+1+k}, T_{E+1+2k,1+k,R}^{\frac{E}{2}+1+k}, T_{E+1+2k,1+k,R}^{\frac{E}{2}+1+k}), \\ & \Delta(T_{E+1+2k,2+k,L}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k,R}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k,R}^{\frac{E}{2}+1+k}), \\ & \Delta(T_{E+1+2k,2+k,L}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k,L}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k,R}^{\frac{E}{2}+1+k}, T_{E+1+2k,2+k,R}^{\frac{E}{2}+1+k})). \end{split}$$

Fourth, in examining the rows to which the arguments of this last equation belong, using Lemma 16.1 we have

- In a 10.1 we have $T_{E+1+2k,1+k,L}^{\frac{2}{2}+1+k} \text{ in row } E,$ $T_{E+1+2k,1+k,R}^{\frac{2}{2}+1+k} \text{ in row } E-1,$ $T_{E+1+2k,1+k,L}^{\frac{2}{2}+1+k} \text{ in row } E-2, \text{ and}$ $T_{E+1+2k,2+k,L}^{\frac{2}{2}+1+k} \text{ in row } E-3.$

The proof is completed by the induction assumption applied to rows E-1, E-2, E-3. More specifically we must show that when k=0 the second row element is a function of the leftmost diagonal elements on rows of index less than or equal to E. But the first argument on the right hand side is the leftmost diagonal element of row E while the induction assumption assures us that the other arguments are functions of the leftmost elements of previous rows, e < E. This completes the proof.

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