

# The category TOF

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We provide a complete set of identities for the symmetric monoidal category, TOF, generated by the Toffoli gate and computational ancillary bits. We do so by demonstrating that the functor which evaluates circuits on total points, is an equivalence into the full subcategory of sets and partial isomorphisms with objects finite powers of the two element set. The structure of the proof builds – and follows the proof of Cockett et al. – which provided a full set of identities for the cnot gate with computational ancillary bits. Thus, first it is shown that TOF is a discrete inverse category in which all of the identities for the cnot gate hold; and then a normal form for the restriction idempotents is constructed which corresponds precisely to subobjects of the total points of TOF. This is then used to show that TOF is equivalent to  $\text{FPinj}_2$ , the full subcategory of sets and partial isomorphisms in which objects have cardinality  $2^n$  for some  $n \in \mathbb{N}$ .

## 1 Introduction

The Toffoli gate is a cornerstone for reversible computing: alongside the Fredkin gate, it was the first gate which was proven to be universal for classical reversible computing [6]. That is, if the values of certain wires are fixed and unchanged by computation (called “auxiliary bits”) and the value of others ignored after computation (called “garbage bits”), then every reversible boolean function can be simulated using either Fredkin or Toffoli gates. The universality of the Fredkin gate relies fundamentally on the use of garbage bits. The Toffoli gate, however, is universal for classical reversible circuits even when *only* auxiliary bits are allowed. This is notable, as even the Fredkin and cnot gates combined are not universal in this sense [1, Thm. 3]. Moreover, the Toffoli gate is also used frequently in quantum computation (especially in quantum error correction [15, 9]); and has even been physically realized [13, 14].

Notably, [11] provided an infinite, complete set of identities for functions of the form  $|x_1, \dots, x_n, y\rangle \mapsto |x_1, \dots, x_n, y + f(x_1, \dots, x_n)\rangle$  generated by Toffoli gates with finitely many control wires, along with finitely many qubits in the state  $|0\rangle$ .

Auxiliary bits are a rather peculiar notion and are unnatural in the context of symmetric monoidal categories<sup>1</sup>; instead, we shall take a more general and richer approach by simply allowing the inputs and outputs of wires to be fixed by components which we call “ancillary bits”. Ancillary bits, in this sense, are modelled by qubit initialization and termination in [10], where the state of a circuit can degenerate to an undefined state – in finite dimensional Hilbert spaces,  $\text{FHilb}$ , this corresponds to when maps compose to a zero-matrix.

In [5], Cockett et al. provided a complete set of identities for the fragment of quantum computing generated by the controlled-not gate with ancillary bits. The paper was inspired by Lafont’s work [12],

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<sup>1</sup>An auxiliary bit can be simulated by fixing the input and output of a wire using an input and output ancillary bit. However, usually, there is the added assumption that fixing bits in this manner will not cause the resulting function to degenerate or become partially defined. Thus, auxiliary bits, as commonly formulated, do not provide a compositional notion.

but used the notion of a discrete inverse category developed in [7]. Inverse categories have now been used extensively to model the semantics of reversible computation [2, 8]. The current paper builds on [5] and provides a finite, complete set of identities for the fragment of quantum computing generated by the Toffoli gate with ancillary bits<sup>2</sup>. We call the symmetric monoidal category generated by the Toffoli gate and ancillary bits with these identities, TOF.

In addition to providing a complete set of identities for these circuits, we also prove a concrete equivalence into the subcategory of sets and partial isomorphisms where the objects are finite powers of the 2 element set<sup>3</sup>. The key step of this proof is to prove that the functor  $\tilde{H}_0$ , which takes an object to its (total) points, is faithful: this, in turn, relies on providing a normal form for the restriction idempotents of TOF.

### Overview of the proof

We first present the symmetric monoidal category called TOF which is generated by the Toffoli gate and ancillary bits along with 17 identities. The paper culminates with a proof that TOF is isomorphic to the category,  $\text{FPinj}_2$ , of partial isomorphisms between finite powers of the two element set. The proof follows the form of the proof in [5] for the category CNOT, built from the computational ancillary bits and the cnot gate. We start by observing, Lemma 4.1, that all of the identities of the category CNOT hold in TOF. The first crucial step is to prove that some needed identities of Iwama [11] hold in this setting. The next crucial next step is to prove that TOF is a discrete inverse category. Discrete inverse categories have inverse products and, for TOF, these are essentially inherited from CNOT. Furthermore, in TOF, just as for CNOT, partial inverses are given by horizontally flipping circuits: this gives a dagger functor  $(\_)^\circ : \text{TOF}^{\text{op}} \rightarrow \text{TOF}$ . In Section 8, we construct a discrete inverse functor  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$  which “evaluates” maps on the points of TOF. In Section 9, we construct a normal form for the restriction idempotents of TOF using “polyforms”: this is the crux of the paper. In Section 10 we prove that the functor  $\tilde{H}_0$  is full faithful, borrowing results from [5] – wherein it is shown that  $\tilde{H}_0$  being full and faithful on restriction idempotents implies  $\tilde{H}_0$  is full and faithful in general.

## 2 Restriction and inverse categories

In this section, we recall the basic theory and terminology of restriction and inverse categories which will be used later.

**Definition 2.1.** [4, Def. 2.1.1] A **restriction structure** on a category  $\mathbb{X}$  is an assignment  $\bar{f} : A \rightarrow A$  for each map  $f : A \rightarrow B$  in  $\mathbb{X}$  satisfying the following four axioms:

$$[\mathbf{R.1}] \quad \bar{f}f = f \qquad [\mathbf{R.2}] \quad \bar{f}\bar{g} = \bar{g}\bar{f} \qquad [\mathbf{R.3}] \quad \overline{\bar{f}} = \bar{f}\bar{g} \qquad [\mathbf{R.4}] \quad f\bar{g} = \bar{f}gf$$

A **restriction category** is a category equipped with a restriction structure. A **restriction functor** is a functor which preserves the given restriction structure. An endomorphism  $e : A \rightarrow A$  is called a **restriction idempotent** if  $e = \bar{e}$ . In particular, each  $\bar{f}$  has  $\overline{\bar{f}} = \bar{f}$  and is an idempotent as  $\overline{\bar{f}}\bar{f} = \bar{f}\bar{f} = \bar{f}$ .

In a restriction category, a **total map** is a map  $f$  such that  $\bar{f} = 1$ . The total maps of a restriction category  $\mathbb{X}$  form a subcategory  $\text{Total}(\mathbb{X})$  of  $\mathbb{X}$ .

<sup>2</sup>Although the larger fragment of quantum computing generated by the Toffoli gate, Hadamard gate and computational ancillary bits has recently been classified [16].

<sup>3</sup>This result implies, in particular, that TOF embeds faithfully into the categories of matrices [3],  $\text{Mat}(\mathbb{C})$ , as  $\text{FPinj}_2$  embeds into the category of matrices by taking partial isomorphisms to their adjacency matrices.

A basic example of a restriction category is the category of sets and partial functions,  $\text{Par}$ : the restriction of a partial function is the partial identity on the domain of definition.

**Definition 2.2.** [4, Sec. 2.3]. A map  $f$  is a **partial isomorphism** when there exists another map  $g$ , called the **partial inverse** of  $f$ , such that  $f = fg$  and  $\bar{g} = gf$ . A restriction category  $\mathbb{X}$  is an **inverse category** when all its maps are partial isomorphisms.

Partial isomorphisms generalize the notion of isomorphisms to restriction categories; thus, the composition of partial isomorphisms is a partial isomorphism and partial inverses are unique. Furthermore, every restriction category  $\mathbb{X}$  has a subcategory of partial isomorphisms  $\text{ParIso}(\mathbb{X})$  which is an inverse category. Denote the category  $\text{ParIso}(\text{Set})$  by  $\text{Pinj}$ .

There is an important alternate characterization of an inverse category:

**Theorem 2.3.** [4, Thm. 2.20] A category  $\mathbb{X}$  is an inverse category if and only if there exists a functor  $(-)^{\circ} : \mathbb{X}^{\text{op}} \rightarrow \mathbb{X}$  which is the identity on objects, satisfying the following three axioms:

$$[\text{INV.1}] (f^{\circ})^{\circ} = f \qquad [\text{INV.2}] ff^{\circ}f = f \qquad [\text{INV.3}] ff^{\circ}gg^{\circ} = gg^{\circ}ff^{\circ}$$

Inverse categories have restriction structure given by  $\bar{c} := cc^{\circ}$ . Every idempotent in an inverse category is necessarily a restriction idempotent.

Inverse categories can have a product-like structure:

**Definition 2.4.** [7, Def. 4.3.1]

Take a symmetric monoidal inverse category  $\mathbb{X}$  with tensor  $_{-} \otimes _{-} : \mathbb{X} \times \mathbb{X} \rightarrow \mathbb{X}$ , symmetry  $c$  and associator  $a$ . Suppose moreover that the tensor preserves  $(-)^{\circ}$ . We say  $\mathbb{X}$  has **inverse products** if there exists a total natural diagonal transformation  $\Delta$  which satisfies the following properties:

[DINV.1]  $\Delta$  is cocommutative:

$$\begin{array}{ccc} A & \xrightarrow{\Delta_A} & A \otimes A \\ & \searrow \Delta_A & \downarrow c_{A,A} \\ & & A \otimes A \end{array}$$

[DINV.3]  $(\Delta, \Delta^{\circ})$  is a semi-Frobenius object:

$$\begin{array}{ccc} A \otimes A & \xrightarrow{(\Delta_A \otimes 1_A)a_{A,A,A}} & A \otimes (A \otimes A) \\ \downarrow (1_A \otimes \Delta_A)a_{A,A,A}^{\circ} & \searrow \Delta_A^{\circ} & \downarrow 1_A \otimes \Delta_A^{\circ} \\ (A \otimes A) \otimes A & \xrightarrow{\Delta_A^{\circ} \otimes 1_A} & A \otimes A \end{array}$$

[DINV.2]  $\Delta$  is coassociative:

$$\begin{array}{ccc} A & \xrightarrow{\Delta_A} & A \otimes A \\ \Delta_A \downarrow & & \downarrow 1_A \otimes \Delta_A \\ A \otimes A & & A \otimes (A \otimes A) \\ & \searrow \Delta_A \otimes 1_A & \nearrow a_{A,A,A} \\ & (A \otimes A) \otimes A & \end{array}$$

[DINV.4]  $\Delta$  is uniform-copying:

$$\begin{array}{ccc} A \otimes B & \xrightarrow{\Delta_A \otimes \Delta_B} & (A \otimes A) \otimes (B \otimes B) \\ \Delta_{A \otimes B} \searrow & & \downarrow \begin{array}{l} a_{A,A,B \otimes B}(1_A \otimes a_{A,B,B}^{\circ}) \\ (1_A \otimes (c_{A,B} \otimes 1_B)) \\ ((1_A \otimes a_{B,A,B})a_{A,B,A}^{\circ}) \end{array} \\ & & (A \otimes B) \otimes (A \otimes B) \end{array}$$

A **discrete inverse category** is a category with inverse products. Note that  $\Delta$  is required to be total, so that  $\Delta \Delta^{\circ} = 1$ : making the semi-Frobenius structure separable (or special).

### 3 The category CNOT

In [5] the category CNOT was presented as a symmetric monoidal category with objects natural numbers generated by the 1 ancillary bits  $|1\rangle \equiv \blacktriangleright$ , and  $\langle 1| \equiv \blacktriangleleft$ <sup>4</sup>, and the controlled not gate  $\text{cnot} \equiv \text{---}\oplus\text{---}$ , where these gates satisfy the identities in Figure 1:

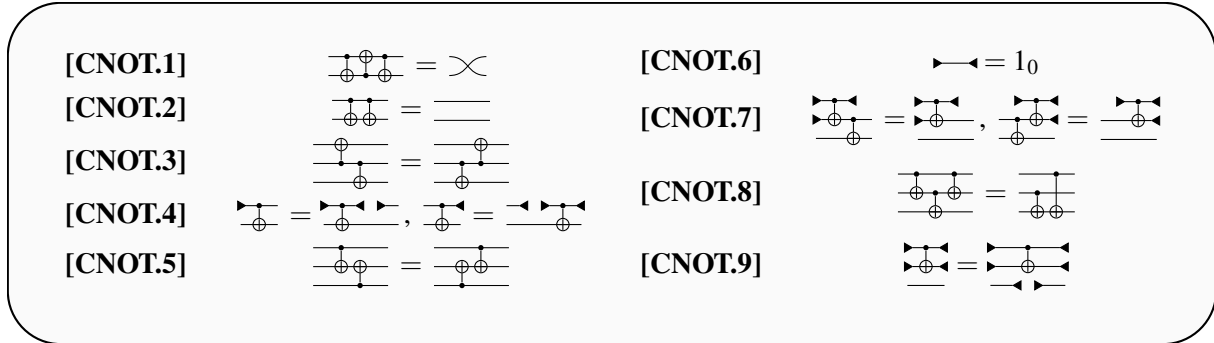


Figure 1: The identities of CNOT

Notice that there are “gaps” in some of the cnot gates, and others are flipped. This is just shorthand to suppress burdensome symmetry maps. There is an obvious interpretation of this notation, for example:

$$\text{---}\oplus\text{---} := \text{---}\oplus\text{---}$$

It was shown that these identities were complete; and moreover, it was shown that CNOT is equivalent to the category of affine partial isomorphisms between finite  $\mathbb{Z}_2$ -vector spaces. We shall use these observations to obtain a similar equivalence for the category, TOF, generated by the Toffoli gate and 1 ancillary bits.

### 4 The category TOF

Define the category TOF to be the symmetric monoidal category, with objects the natural numbers, generated by the 1 ancillary bits  $|1\rangle$  and  $\langle 1|$  (depicted graphically as in CNOT) as well as the Toffoli gate:

$$\text{tof} := \text{---}\oplus\text{---}$$

The Toffoli gate and the 1-ancillary bits allow the cnot gate, not gate,  $|0\rangle$  gate,  $\langle 0|$  gate, flipped tof gate and flipped cnot gate to be succinctly defined:

$$\text{---}\oplus\text{---} := \text{---}\oplus\text{---}, \quad \oplus := \text{---}\oplus\text{---}, \quad \blacktriangleright := \blacktriangleright \oplus, \quad \blacktriangleleft := \oplus \blacktriangleleft, \quad \text{---}\oplus\text{---} := \text{---}\oplus\text{---}, \quad \oplus := \text{---}\oplus\text{---}$$

We also allow for “gaps” in tof and cnot gates, as in CNOT.

<sup>4</sup>Here, the bra-ket notation is slightly deceptive because the diagrammatic order of composition makes the inner product look like the outer product, and vice versa.

These gates must satisfy the identities given in Figure 2:

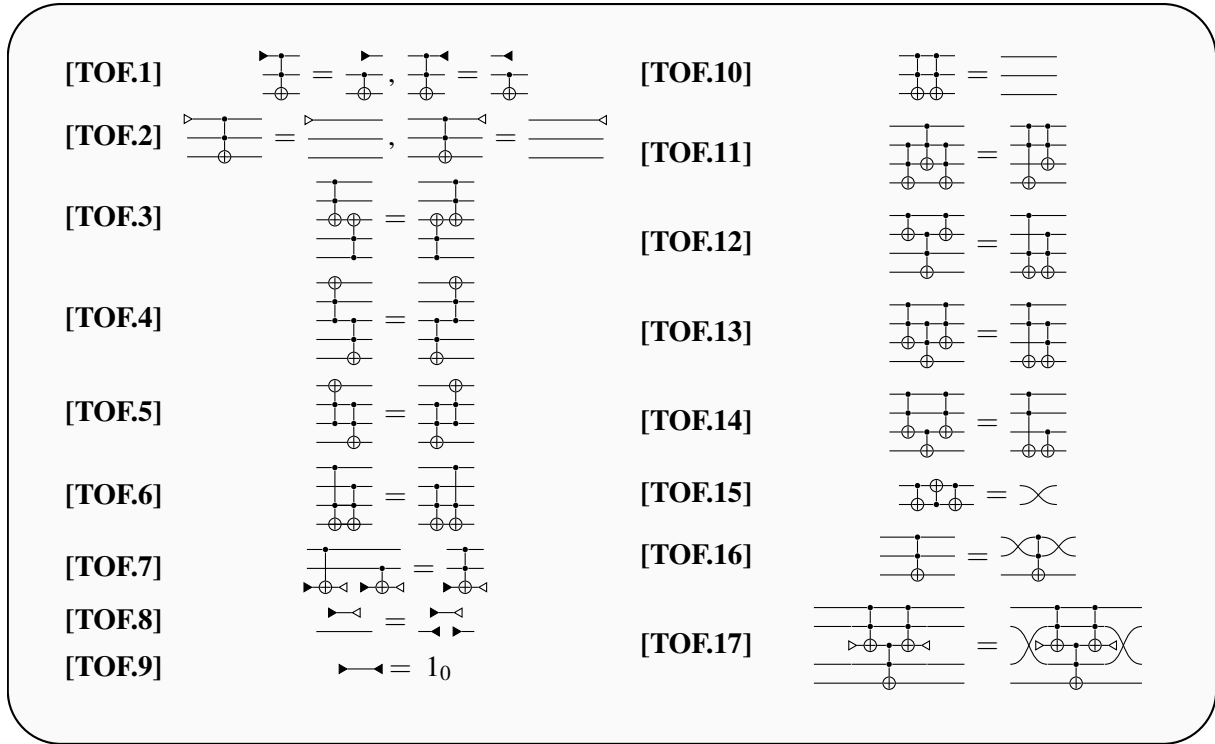


Figure 2: The identities of TOF

Axioms [TOF.1]-[TOF.6], [TOF.9]-[TOF.10] are relatively intuitive. [TOF.8] corresponds to tensoring a matrix with the  $1 \times 1$  zero matrix: this is useful for establishing the restriction structure of TOF. [TOF.7] is used for establishing a normal form for the restriction idempotents. Any of Axioms [TOF.11]-[TOF.14] combined with [TOF.10] are used to push cnot/tof gates past each other thereby generating another trailing cnot/tof gate: this will be discussed in more detail in Lemma 5.5 ( $\nu$ ). [TOF.15] is inherited from CNOT and is used to establish the inverse products of TOF. [TOF.16] expresses the commutativity of multiplication for the Toffoli gate. [TOF.17] is similar to [TOF.16], except for the 3-bit-controlled-not gate.

As an exercise, we first show that  $|0\rangle\langle 0| = 1_0$  (just as  $|1\rangle\langle 1| = 1_0$  in [TOF.9]):

$$\triangleright \triangleleft = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = \begin{array}{c} \text{---} \text{---} \text{---} \\ \oplus \oplus \oplus \end{array} = 1_0$$

Since we build on the work done in [5], we start by establishing that there is a canonical functor from the category CNOT into TOF. The proof is contained in Appendix A.

**Lemma 4.1.** The canonical interpretation of CNOT in TOF is functorial.

## 5 Controlled not gates and the Iwama identities

In reversible and quantum computing it is usual to regard the Toffoli gate and cnot as the not gate “controlled”, respectively, by one and two control wires. In TOF one can define  $\text{cnot}_n$  the not gate controlled by  $n$  wires for any  $n \in \mathbb{N}$ :

**Definition 5.1.** For every  $n \in \mathbb{N}$ , inductively define the controlled not gate,  $\text{cnot}_n : n + 1 \rightarrow n + 1$  inductively by:

- For the base cases, let  $\text{cnot}_0 := \text{not}$ ,  $\text{cnot}_1 := \text{cnot}$  and  $\text{cnot}_2 := \text{tof}$ .
- For all  $n \in \mathbb{N}$  such that  $n \geq 2$ :

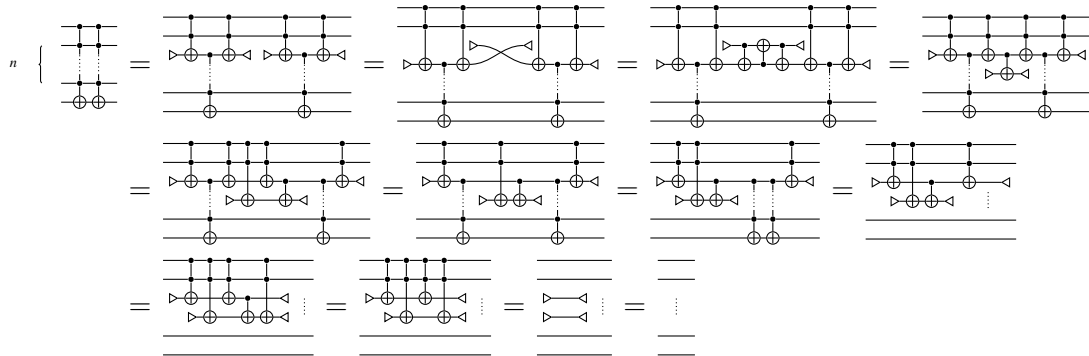
$$\text{cnot}_{n+1} \equiv \left. \begin{array}{c} \text{---} \\ | \\ \text{---} \\ | \\ \text{---} \\ | \\ \oplus \end{array} \right\}^n := \begin{array}{c} \text{---} \\ | \\ \text{---} \\ | \\ \text{---} \\ | \\ \oplus \end{array}$$

The first  $n$  wires of  $\text{cnot}_n$  are the **control wires** while the last wire is the **target wire**. As for the cnot and tof gates, allow for  $\text{cnot}_n$  gates to have “gaps” in between the control wires (and the target wire).

Notice that  $\text{cnot}_n$  gates are self inverse:

**Lemma 5.2.** Every  $\text{cnot}_n$  gate is self-inverse.

*Proof.* The base cases are trivial. For the inductive step, suppose that  $\text{cnot}_n$  gates are self-inverse, then:



□

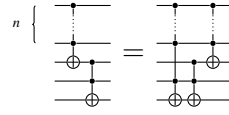
Next we show how we can “unzip”  $\text{cnot}_n$  gates; and simultaneously, we generalize Lemma B.1 to show how a  $\text{cnot}_n$  gate can be “pushed” past a Toffoli gate. This is a crucial observation in establishing the Iwama identities. The proof is contained in Appendix B.

**Proposition 5.3.** Given some  $n \geq 1$  and  $k \geq 1$ :

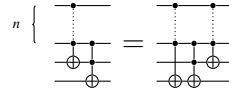
- (i)  $\text{cnot}_{n+k}$  gates can be zipped and unzipped:

$$\left. \begin{array}{c} \text{---} \\ | \\ \text{---} \\ | \\ \text{---} \\ | \\ \oplus \end{array} \right\}^n = \left. \begin{array}{c} \text{---} \\ | \\ \text{---} \\ | \\ \text{---} \\ | \\ \oplus \end{array} \right\}^k$$

(ii)  $\text{cnot}_n$  gates can be pushed past Toffoli gates in the following sense:

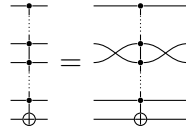


(iii) And likewise:



We can use the ability to zip and unzip controlled not gates to show that we can transpose control wires:

**Corollary 5.4.**



*Proof.* The claim follows vacuously for  $n < 2$ . The base case when  $n = 2$  follows immediately from [TOF.16]. Suppose that the claim holds for some  $\text{cnot}_n$ . Consider a  $\text{cnot}_{n+1}$  gate and unzip this gate one level down. We can transpose the top two control wires by the base case and we can transpose the bottom  $n - 1$  control wires by the inductive hypothesis. Finally, to transpose the second and third wire from the top, either unzip the  $\text{cnot}_{n+1}$  gate down by one more step. If this cannot be done – because there is nothing more to unzip – it means one can directly apply [TOF.17].  $\square$

Since transpositions generate the symmetric group, the control wires of a  $\text{cnot}_n$  gate can, therefore, be freely permuted.

We use the notation  $\oplus_x^X$  to denote a  $\text{cnot}_{|X|}$  gate operating on the control wires in the set  $X$  and targeting the wire  $x$ . Similarly let  $\triangleright_x$  and  $\triangleleft_x$  be, respectively, the  $|0\rangle$  and  $\langle 0|$  gates on the wire  $x$ .

The following identities are due to Iwama et al. [11]. They show how to push generalized control not gates past each other in certain key situations. The proof is contained in Appendix C.

**Lemma 5.5** (Iwama’s Identities).

- (i)  $\oplus_x^X \oplus_x^X = 1$
- (ii) When  $x \in X$  then  $\triangleright_x \oplus_y^X = \triangleright_x$
- (iii) When the target wire are the same  $\oplus_x^X \oplus_x^Y = \oplus_x^Y \oplus_x^X$
- (iv) When  $x \notin Y$  and  $y \notin X$  then  $\oplus_x^X \oplus_y^Y = \oplus_y^Y \oplus_x^X$
- (v)  $\oplus_x^X \oplus_y^{\{x\} \sqcup Y} = \oplus_y^{X \cup Y} \oplus_y^{\{x\} \sqcup Y} \oplus_x^X$
- (vi)  $\triangleright_z \oplus_z^{\{x\}} \oplus_y^{\{x\} \sqcup X} = \triangleright_z \oplus_z^{\{x\}} \oplus_y^{\{z\} \sqcup X}$

## 6 TOF is a discrete inverse category

Next we prove that TOF is a discrete inverse category.

Define  $(-)^{\circ} : \text{TOF}^{\text{op}} \rightarrow \text{TOF}$  to be the contravariant functor which flips circuits horizontally, mapping  $|1\rangle \mapsto \langle 1|$ ,  $\langle 1| \mapsto |1\rangle$  and  $\text{tof} \mapsto \text{tof}$ . Clearly  $(-)^{\circ}$  is an involution (and thus, a dagger functor) as all of the axioms are horizontally symmetric and  $(f^{\circ})^{\circ} = f$ .

The diagonal map in TOF is the image of the diagonal map,  $\Delta_n : n \rightarrow n \otimes n$ , in CNOT under the canonical functor  $\text{CNOT} \rightarrow \text{TOF}$ :

**Definition 6.1.** Define a family of maps  $\Delta := \{\Delta_n\}_{n \in \mathbb{N}}$  with the cnot gate and 1-ancillary wires inductively, as in [5], such that  $\Delta_0 = 1_0$ ,

$$\Delta_1 := \text{---} \langle \text{---} := \text{---} \oplus \text{---} \quad \text{and for all } n > 1: \quad \Delta_n := \text{---} \langle \text{---} := \text{---} \overset{n-1}{\langle \text{---}}$$

This yields the following result; the proof is contained in Appendix D:

**Proposition 6.2.** TOF is a discrete inverse category with this structure.

## 7 The points of TOF

The (total) points of TOF are iterated tensors of the input ancillary bits ( $|0\rangle$  and  $|1\rangle$ ) and can be denoted using the “ket” notation. We start by observing that the Toffoli gate in TOF behaves as expected on these points; the proof is contained in Appendix E:

**Lemma 7.1.**  $\text{tof}(|x_1, x_2, x_3\rangle) = |x_1, x_2, x_1 \cdot x_2 + x_3\rangle$

The points above which are expressed in bra notation are clearly total. However, not all maps with domain 0 are total: in particular, as in CNOT, the maps  $\Omega_{0,m}$  for  $m \in \mathbb{N}$  are not total:

**Definition 7.2.** Define  $\Omega := |1\rangle\langle 0|$ , and  $\Omega_{n,m} := \langle 1|^{\otimes n} \Omega |1\rangle^{\otimes m}$ .

When any map is tensored with such a map it becomes one of them:

**Lemma 7.3.** For all circuits  $f \in \text{TOF}(n, m)$ ,  $f \otimes \Omega = \Omega_{n,m}$ .

*Proof.* By the functionality of the interpretation of CNOT into TOF and [5, Lemma A.2] note that  $\Omega$  is idempotent. Therefore, use [TOF.8] to cut all of the wires around all Toffoli gates. Notice that when the wires round a Toffoli gate is cut this results in the map:

$$|111\rangle \text{tof} \langle 111| = |11\rangle \text{cnot} \langle 11| = |1\rangle \text{not} \langle 1| = |0\rangle \langle 1| =: \Omega$$

When all wires of a circuit are cut it will therefore take the form:

$$\langle 1|^{\otimes n} (\Omega)^{\otimes k} (|1\rangle \langle 1|)^{\otimes \ell} |1\rangle^{\otimes m} = \langle 1|^{\otimes n} (\Omega)^{\otimes k} (1_0)^{\otimes \ell} |1\rangle^{\otimes m} = \langle 1|^{\otimes n} \Omega |1\rangle^{\otimes m} =: \Omega_{n,m}$$

□

In fact, there are only two sorts of points, those that are expressible as kets—and maps of the form  $\Omega_{0,n}$  for some  $n \in \mathbb{N}$ :

**Lemma 7.4.** For all  $n \in \mathbb{N}$  and  $f \in \text{TOF}(0, n)$  implies  $f = \Omega_{0,n}$  or  $f = |b_1, \dots, b_n\rangle$  for some  $b_1, \dots, b_n \in \mathbb{Z}_2$ .



*Proof.* Given any circuit  $f \in \text{TOF}(0, n)$ , pull all of the  $|1\rangle$  gates to the left of the circuit and all of the  $\langle 1|$  gates to the right. In the middle, there will only be tof gates; thus, apply the  $|1\rangle$  gates to the tof gates using Lemma 7.1. This will result in a series of  $|1\rangle$  and  $|0\rangle$  gates on the left. Repeatedly apply these  $|1\rangle$  and  $|0\rangle$  gates to the tof gates using Lemma 7.1 until there are only a series of  $|1\rangle$  and  $|0\rangle$  gates on the left and a series of  $\langle 1|$  gates on the right, and nothing in the middle. If a  $|1\rangle$  and  $\langle 1|$  gate meet, they compose to form the identity and disappear. If this process can eliminate all of the  $\langle 1|$  gates, then we are done. Otherwise, if  $|0\rangle$  and  $\langle 1|$  gate meet, they compose to form  $\Omega$ ; therefore, by Lemma 7.3,  $f = \Omega_{0,n}$ .  $\square$

## 8 Partial injective functions and TOF

For any restriction category  $\mathbb{X}$  and any object  $X \in \mathbb{X}$ , we may define a restriction-preserving hom-functor  $h_X : \mathbb{X} \rightarrow \text{Par}$  by:

**On Objects:**  $h_X(Y) := \{f \in \mathbb{X}(X, Y) \mid \bar{f} = 1_X\}$

**On Maps:** For each map  $f : Y \rightarrow Z$ , for all  $g \in h_X(Y)$ :

$$(h_X(f))(g) := \begin{cases} gf & \text{if } \bar{g}f = 1_X \\ \uparrow & \text{otherwise} \end{cases}$$

As TOF is a restriction category, we may consider the functor  $h_0 : \text{TOF} \rightarrow \text{Par}$  which evaluates circuits on the total points of TOF. As TOF is an inverse category and this functor preserves restriction, we can lift  $h_0 : \text{TOF} \rightarrow \text{Par}$  to a restriction-preserving functor  $H_0 : \text{TOF} \rightarrow \text{Pinj}$ . Clearly the nature of the total points of TOF ensures that the discrete inverse structure is preserved by  $H_0$ :

**Lemma 8.1.**  $H_0 : \text{TOF} \rightarrow \text{Pinj}$  preserves discrete products.

*Proof.*  $H_0(n) = \{|b_1 \dots b_n\rangle \mid b_i \in \{0, 1\}\}$  so that  $H_0(n+m) \simeq H_0(n) \times H_0(m)$  and  $H_0(\Delta) = \Delta$ .  $\square$

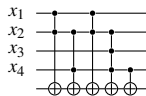
Define  $\text{FPinj}_2$  to be the full subcategory of  $\text{Pinj}$  (sets and partial isomorphisms) with objects finite powers of the two element set. By Lemma 7.4, the object  $n \in \text{TOF}$  under  $H_0$  corresponds to the set  $\{|0\rangle, |1\rangle\}^n$  in  $\text{FPinj}_2$ ; therefore, by restricting the co-domain of  $H_0$  we obtain a bijective on objects functor  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$ . We will prove that this functor is an equivalence of categories.

## 9 A normal form for the idempotents of TOF

As in [5], our objective is to reduce the fullness and faithfulness of  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$  to its fullness and faithfulness on restriction idempotents.  $\text{cnot}_n$  gates will be used to define a class of circuits which will allow a normal form for the idempotents of TOF to be established. It is also worth noting that the following class of circuits corresponds to the canonical form of [11, Def. 4]:

**Definition 9.1.** A circuit  $f : n \rightarrow n$  is said to be in **polynomial form** when it is the composition of circuits  $f = c_1 \cdots c_k$  where each  $c_i$  is a  $\text{cnot}_j$  gate with control any of the first  $n - 1$  wires and target the last wire.

For example, the following circuit corresponding to the polynomial  $x_1x_2 + x_2x_4 + x_1x_2 + x_1x_2 + x_2x_3x_4 + x_4$  is in polynomial form:

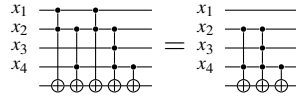


Clearly all polynomials (in normal form) can be represented by a circuit in polynomial form, where each monomial corresponds to a  $\text{cnot}_n$  gate. Moreover, this correspondence is bijective, as  $\text{cnot}_n$  gates targeting the same wire obviously commute, and  $\text{cnot}_n$  gates are self-inverse by Lemma 5.2.

For example, the following identity holds in  $\mathbb{Z}_2[x_1, x_2, x_3, x_4]$

$$x_1x_2 + x_2x_4 + x_1x_2 + x_2x_3x_4 + x_4 = x_2x_4 + x_2x_3x_4 + x_4$$

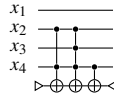
and this corresponds to the following identity in TOF:



Since the restriction idempotents in  $\text{FPinj}$  are determined by boolean equations or, equivalently, by the zeros of multivariate polynomials over  $\mathbb{Z}_2$ , we use polynomial form circuits to provide a normal form for the restriction idempotents of TOF. By restricting the value of the bottom wire to be 0, the evaluation of this circuit on total points is defined if and only if the corresponding tuple of bits is in the kernel of the function corresponding to polynomial evaluation:

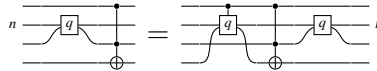
**Definition 9.2.** A circuit  $e : n \rightarrow n$  in TOF is a **polyform** if  $e = (1_n \otimes |0\rangle)q(1_n \otimes \langle 0|)$  for some  $q : n + 1 \rightarrow n + 1$  in polynomial form.

For example the following circuit corresponding to the equation  $x_2x_4 + x_2x_3x_4 + x_4 = 0$  is a polyform:



Given a polynomial form circuit  $q$ , let the circuit  $\overline{q}$  denote the circuit where every  $\text{cnot}_n$  gate of  $q$  is additionally controlled by wire with the black dot.

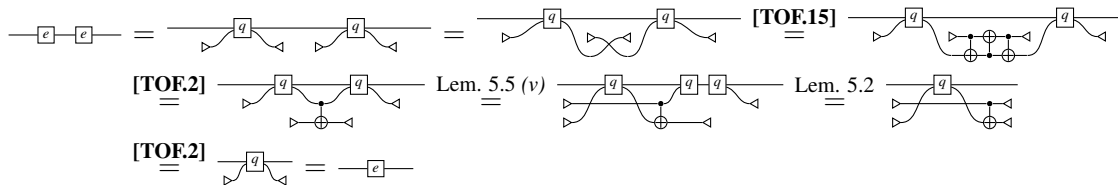
In fact given a polynomial form circuit  $q : n + 1 \rightarrow n + 1$ , by the repeated application of Lemma 5.5 (v) for each constituent  $\text{cnot}_n$  gate, it is useful to observe:



Our aim is now to show that the idempotents of TOF are always equivalent to a polyform so that these circuits provide a normal form for the idempotents. Because some of the steps are subtle, we annotate certain equalities with the corresponding identities used.

**Lemma 9.3.** Polyforms are idempotents (and therefore restriction idempotents).

*Proof.* Consider some map  $e := (1_n \otimes |0\rangle)q(1_n \otimes \langle 0|)$  a polyform, as above, then:



□

We prove that the converse also holds using 3 lemmas:

**Proposition 9.4.** All idempotents in TOF are equivalent to a circuit which is a polyform.

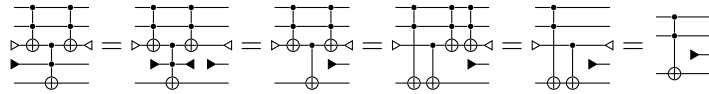
Given a polyform,  $f$ , by structural induction, it suffices to show that sandwiching  $f$  with a generating gate,  $g$ , to obtain  $(1 \otimes g \otimes 1)f(1 \otimes g^\circ \otimes 1)$ , always results in a circuit which has a polyform. There are 3 cases, one for each generating circuit:

**Lemma 9.5.** If  $f : n \rightarrow n$  is a polyform then sandwiching  $f$  by a tof gate results in a circuit with a polyform.

*Proof.* By Lemma 5.5 (v), push  $g$  through  $f$  until it meets the other  $g^\circ = g$  and then annihilate both generalized controlled not gates by Lemma 5.2. This circuit is still a polyform.  $\square$

**Lemma 9.6.** If  $f : n \rightarrow n$  is a polyform then sandwiching  $f$  by a  $|1\rangle$  gate results in a circuit equivalent to a polyform.

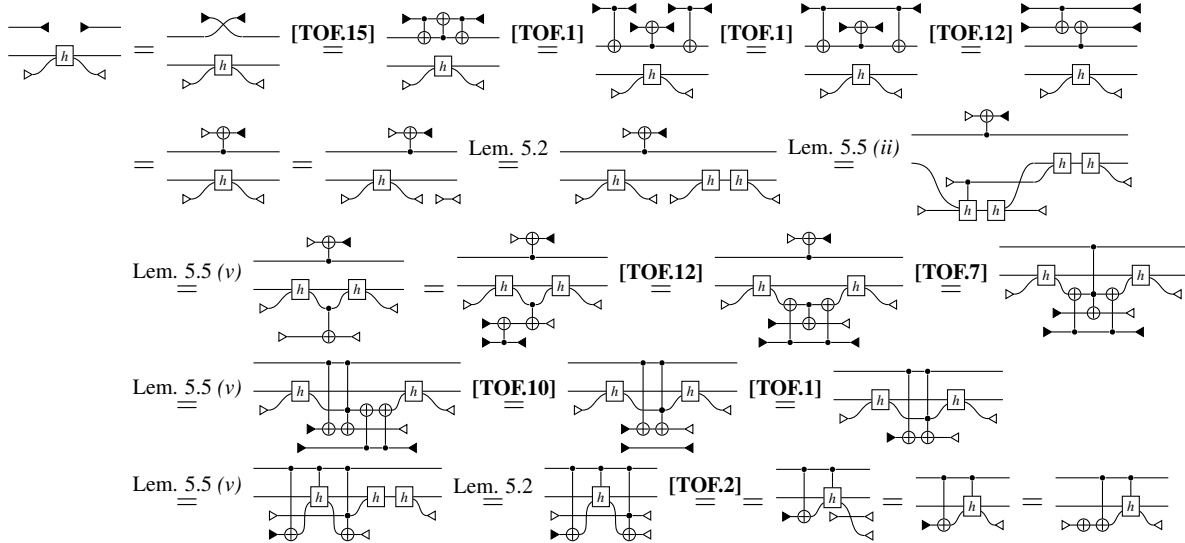
*Proof.* It suffices to observe for the inductive step that:



$\square$

**Lemma 9.7.** If  $f : n \rightarrow n$  is a polyform then sandwiching  $f$  by a  $|1\rangle$  and  $\langle 1|$  gate results in a circuit with a polyform.

*Proof.* Suppose  $f : n \rightarrow n$  is a polyform  $(1_n \otimes |0\rangle \otimes 1_m)h(1_n \otimes \langle 0| \otimes 1_m)$ , then:



$\square$

We have now established with Lemma 9.3 and Proposition 9.4:

**Proposition 9.8.** Polyforms are a normal form for the idempotents in TOF.

This implies the desired result:

**Corollary 9.9.**  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPin}_2$  is full and faithful on restriction idempotents.

## 10 Full and faithfulness of $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$

We follow the approach of [5] to prove the fullness of  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$ . First we show that we can “simulate” all the total maps in  $\text{FPinj}_2$  with extra outputs:

**Lemma 10.1.** For every total map  $f \in \text{FPinj}_2(n, m)$ , there is some  $g \in \text{TOF}$  such that  $\tilde{H}_0(g) = \langle 1_{\mathbb{Z}_2^n}, f \rangle$ .

*Proof.* Consider a total  $f \in \text{FPinj}_2(n, m)$ . For any  $i$  such that  $1 \leq i \leq m$ , observe that  $f\pi_i$  corresponds to a polynomial in  $\mathbb{Z}[x_1, \dots, x_n]$  and thus, there is a circuit  $g_i : n + 1 \rightarrow n + 1$  in polynomial form such that  $\tilde{H}_0(h_i) = \langle 1_{\mathbb{Z}_2^n}, f\pi_i \rangle$  where  $h_i := (1_n \otimes |0\rangle)g_i$ .

Now, inductively define the circuit  $P_i$  for all  $i$  such that  $0 \leq i \leq m$ , such that:  $P_0 = 1_n$  and for every  $i$  such that  $0 \leq i < m$ ,  $P_{i+1} := h_{i+1}(P_i \otimes 1_1)$ .

Then  $\tilde{H}_0(P_m) = \langle 1_{\mathbb{Z}_2^n}, f\pi_1, \dots, f\pi_m \rangle = \langle 1_n, f \rangle$ .  $\square$

Then we recall a technical observation from [5]:

**Lemma 10.2** ([5, Lemma C.21]). Let  $F : \mathbb{X} \rightarrow \mathbb{Y}$  be an inverse product preserving functor between discrete inverse categories. Let  $f$  be a partial isomorphism in  $\mathbb{Y}$ . If  $\langle \bar{f}, f \rangle := \Delta(\bar{f} \otimes f)$  and  $\langle \bar{f}^\circ, f^\circ \rangle := \Delta(\bar{f}^\circ \otimes f^\circ)$  are in the image of  $F$ , then  $f$  and  $f^\circ$  are also in the image of  $F$ .

This allows us to show:

**Proposition 10.3.**  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$  is full.

*Proof.* Consider a map  $f \in \text{FPinj}_2(\mathbb{Z}_2^n, \mathbb{Z}_2^m)$  for arbitrary  $n, m \in \mathbb{Z}$ . By Lemma 10.2, if we can simulate  $\langle \bar{f}, f \rangle$  and  $\langle \bar{f}^\circ, f^\circ \rangle$ , we can simulate  $f$ . However, partial maps of the form  $\langle \bar{g}, g \rangle : X \rightarrow X \times Y$  are restrictions of a total map, unless  $Y$  is empty. The case of  $Y$  being empty does not occur in  $\text{FPinj}_2$  as the empty set is not an object. Thus, all such maps are restrictions of total maps. Therefore, by Lemma 10.1 there is some  $h \in \text{TOF}$  such that  $\tilde{H}_0(h) \geq \langle 1, g \rangle$  for any  $g$ . However, by Proposition 9.9  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$  is full on restriction idempotents, so there is some  $e = \bar{e}$  such that  $H_0(e) = \bar{g}$  and so  $H_0(eh) = \langle \bar{g}, g \rangle$  which completes the proof.  $\square$

The faithfulness of  $\tilde{H}_0$  is reduced to its faithfulness on restriction idempotents. This uses another technical result from [5] which we recall:

**Lemma 10.4** ([5, Lemma C.25]). A restriction functor  $F : \mathbb{X} \rightarrow \mathbb{Y}$  between discrete inverse categories, which preserves inverse products, is faithful if and only if it is faithful on restriction idempotents.

As  $\tilde{H}_0$  is faithful on idempotents and preserves inverse products, this gives:

**Proposition 10.5.**  $\tilde{H}_0 : \text{TOF} \rightarrow \text{FPinj}_2$  is faithful.

By Propositions 10.3 and 10.5, we may conclude:

**Theorem 10.6.** TOF is discrete-inverse-equivalent to  $\text{FPinj}_2$ .

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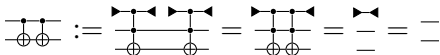
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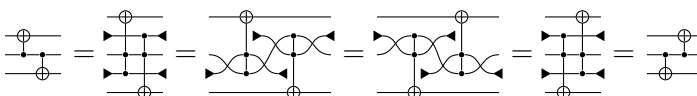
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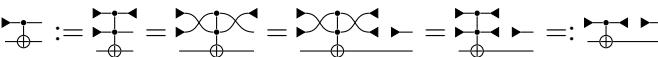
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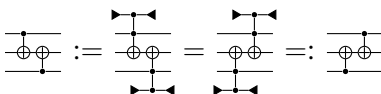
## A Proof of Lemma 4.1

[CNOT.1]: This follows immediately from [TOF.15].

[CNOT.2]: 

[CNOT.3]: 

[CNOT.4]: 

[CNOT.5]: 

[CNOT.6]: This follows immediately from [TOF.9].

[CNOT.7]:

[CNOT.8]:

[CNOT.9]: This follows immediately from [TOF.10].

### B Proof of Proposition 5.3

First, we show how tof gates can be “pushed past” each other with a “trailing” side-effect:

**Lemma B.1.**

(i)

(ii)

*Proof.* Of part (ii):

□

Recall the statement of Proposition 5.3:

Given some  $n \geq 1$  and  $k \geq 1$ :

(i)  $\text{cnot}_{n+k}$  gates can be zipped and unzipped:

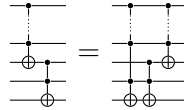
(ii)  $\text{cnot}_n$  gates can be pushed past Toffoli gates in the following sense:

(iii) And likewise:

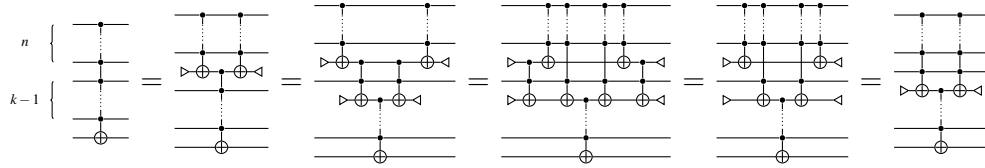
The proof is by a simultaneous induction for claims (i) and (ii) on the number of control wires,  $n$ , to unzip and the number of control wires being pushed past a Toffoli gate. Claim (iii) follows as a consequence.

*Proof.* For the induction, suppose that  $n, k \geq 2$ . The cases when  $n = 1$  or  $n = 2$  follow as a consequence.

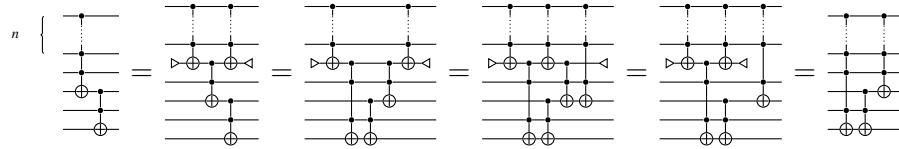
- The base cases of claim (i) follows by the definition of the  $\text{cnot}_n$  gate. The base cases of claim (ii) Lemma is precisely Lemma B.1(ii).
- For  $n \geq 2$ : assume that for any  $m > n$  and all  $k \leq n$ , we can unzip a  $\text{cnot}_m$  gate  $k$  levels down that and we can push a  $\text{cnot}_n$  gate past a tof gate as follows:



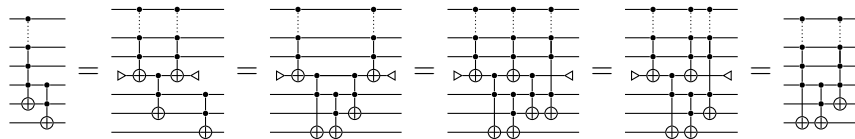
Consider the two identities for  $n + 1$ ; first for the zipper:



For the second inductive step, observe:



Observe that the claim holds when not both  $k \geq 2$  and  $n \geq 2$ , as a consequence of the induction. For claim (iii), we use claims (i) and (ii) to observe:



□

## C Proof of Lemma 5.5

Recall the statement of Lemma 5.5:

(i)  $\oplus_x^X \oplus_x^X = 1$

(ii) When  $x \in X$  then  $\triangleright_x \oplus_y^X = \triangleright_x$

(iii) When the target wire are the same  $\oplus_x^X \oplus_x^Y = \oplus_x^Y \oplus_x^X$

(iv) When  $x \notin Y$  and  $y \notin X$  then  $\oplus_x^X \oplus_y^Y = \oplus_y^Y \oplus_x^X$

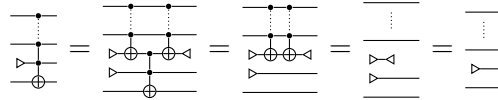
(v)  $\oplus_x^X \oplus_y^{\{x\} \sqcup Y} = \oplus_y^{X \cup Y} \oplus_y^{\{x\} \sqcup Y} \oplus_x^X$

(vi)  $\triangleright_z \oplus_z^{\{x\}} \oplus_y^{\{x\} \sqcup X} = \triangleright_z \oplus_z^{\{x\}} \oplus_y^{\{z\} \sqcup X}$

We prove each case separately:

(i) This is Lemma 5.2.

(ii) Using Corollary 5.4 it suffices to consider the case where  $x$  is the bottom control wire. Using Proposition 5.3 (i) and Lemma 5.2, observe:



(iii) The proof follows easily from Axioms [TOF.3], [TOF.4], [TOF.5] and [TOF.6].

(iv) The proof follows easily from Axioms [TOF.4] and [TOF.5].

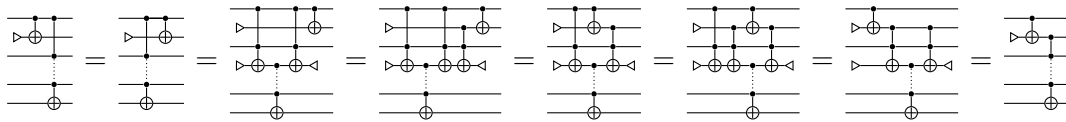
(v) The proof is by induction on the number of control wires of the second gate.

The base cases are provided by Proposition 5.3 (ii) and (iii).

Suppose now that the claim holds for all cases in which the second gate has no more than  $n$  control wires. Consider when the second gate has  $n + 1$  control wires. Using Corollary 5.4 it suffices to consider the case where  $y$  is the bottom wire and  $x$  is the second bottom wire:

$$\begin{aligned}
\oplus_x^X \oplus_y^{Y \sqcup \{x\}} &= \oplus_x^X \triangleright_z \oplus_z^Y \oplus_y^{\{z,x\}} \oplus_z^Y \triangleleft_z && \text{Use Prop. 5.3 (i) to unzip } \oplus_y^{Y \sqcup \{x\}} \text{ to the bottom} \\
&= \triangleright_z \oplus_z^Y \oplus_x^X \oplus_y^{\{z,x\}} \oplus_z^Y \triangleleft_z \\
&= \triangleright_z \oplus_z^Y \oplus_y^{X \sqcup \{z\}} \oplus_y^{\{z,x\}} \oplus_x^X \oplus_z^Y \triangleleft_z && \text{Prop. 5.3 (ii), (iii)} \\
&= \triangleright_z \oplus_z^Y \oplus_y^{X \sqcup \{z\}} \oplus_y^{\{z,x\}} \oplus_z^Y \oplus_x^X \triangleleft_z \\
&= \triangleright_z \oplus_z^Y \oplus_y^{X \sqcup \{z\}} \oplus_z^Y \oplus_y^{\{z,x\}} \oplus_y^{Y \sqcup \{x\}} \oplus_x^X \triangleleft_z && \text{Prop. 5.3 (ii), (iii)} \\
&= \triangleright_z \oplus_z^Y \oplus_z^Y \oplus_y^{X \sqcup \{z\}} \oplus_y^{X \cup Y} \oplus_y^{\{z,x\}} \oplus_y^{Y \sqcup \{x\}} \oplus_x^X \triangleleft_z && \text{Ind. Hyp.} \\
&= \triangleright_z \oplus_y^{X \sqcup \{z\}} \oplus_y^{X \cup Y} \oplus_y^{\{z,x\}} \oplus_y^{Y \sqcup \{x\}} \oplus_x^X \triangleleft_z \\
&= \triangleright_z \oplus_y^{X \sqcup \{z\}} \oplus_y^{X \cup Y} \oplus_y^{\{z,x\}} \triangleleft_z \oplus_y^{Y \sqcup \{x\}} \oplus_x^X \\
&= \triangleright_z \oplus_y^{X \cup Y} \triangleleft_z \oplus_y^{Y \sqcup \{x\}} \oplus_x^X && \text{Lem. 5.5 (ii)} \\
&= \triangleright_z \triangleleft_z \oplus_y^{X \cup Y} \oplus_y^{Y \sqcup \{x\}} \oplus_x^X \\
&= \oplus_y^{X \cup Y} \oplus_y^{Y \sqcup \{x\}} \oplus_x^X
\end{aligned}$$

(vi) Using Corollary 5.4, it suffices to observe:





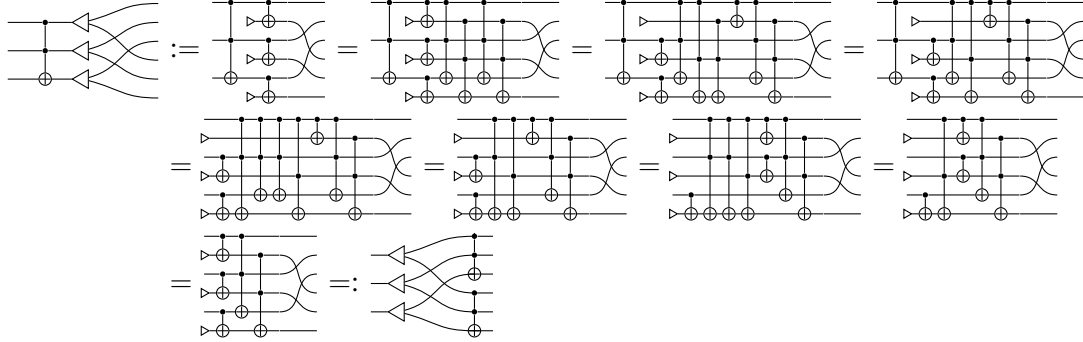
## D Proof of Proposition 6.2

Most of the proof is inherited from [5] using Lemma 4.1.

We first show that  $\Delta$  is a natural transformation:

**Lemma D.1.**  $\Delta$  is a natural transformation in TOF.

*Proof.* We prove that  $\Delta$  is a natural transformation by structural induction. We have only to prove the inductive case for tof as the cases for the 1-ancillary bits are proven in [5, Lemma B.3]:

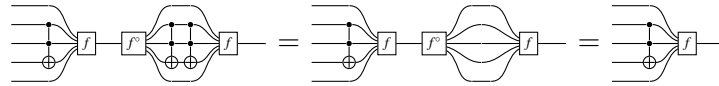


□

To prove that TOF is a discrete inverse category with respect to  $(-)^{\circ} : \text{TOF}^{\text{op}} \rightarrow \text{TOF}$  it must also be shown that [INV.1], [INV.2] and [INV.3] hold. As [INV.1] is immediate it remains to prove [INV.2] and [INV.3]:

**Lemma D.2.** For all maps  $f$  in TOF [INV.2] holds, that is  $ff^{\circ}f = f$ .

*Proof.* We prove that [INV.2] holds by structural induction. Again we have only to prove the inductive case for tof as the cases for the ancillary bits are proven in [5, Lemma B.14]. Suppose inductively that  $ff^{\circ}f = f$  for some circuit  $f$ , then we need to show that we can extend  $f$  by a tof gate and preserve the property. This is almost immediate as:



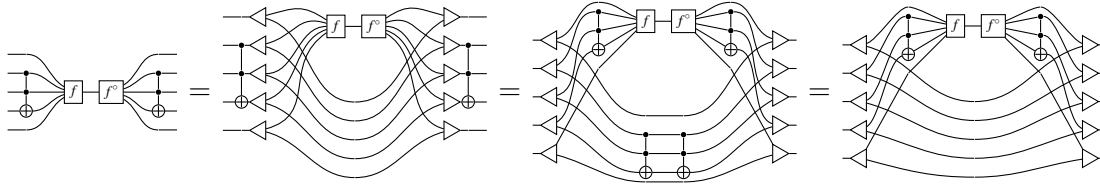
□

To prove that [INV.3] holds, we identify the restriction idempotents of TOF with what are called latchable circuits. A map  $f$  in a discrete, symmetric monoidal category is called **latchable** when  $\Delta(f \otimes 1)\Delta^{\circ}$  [5, Definition B.9]. We already know that latchable circuits commute with each other [5, Lemma B.10]; therefore, to prove that all circuits of the form  $ff^{\circ}$  are latchable is to prove that [INV.3] holds.

**Lemma D.3.** Circuits of the form  $ff^{\circ}$  in TOF are latchable and, thus, [INV.3] holds.

*Proof.* We prove that circuits of the form  $ff^{\circ}$  in TOF are latchable by structural induction. We have only to prove the inductive case for tof as the cases for the 1-ancillary bits are proven in [5, Proposition

B.12]: Suppose that some circuit  $f$  is latchable, then we must show inductively that adjoining a Toffoli gate will result in a latchable circuit:



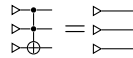
□

This allows us to Proposition 6.2:

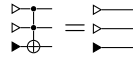
*Proof.* Lemmas D.2 and D.3 show that TOF is an inverse category. Lemma D.1 and the fact that the semi-Frobenius identities hold in CNOT imply that  $(n, \Delta_n, \Delta_n^\circ)$  forms a natural separable, commutative, semi-Frobenius algebra for all  $n \in \mathbb{N}$ . Thus, TOF is a discrete inverse category. □

## E Proof of Lemma 7.1

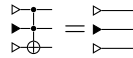
$\text{tof}(|000\rangle) = |000\rangle$ :



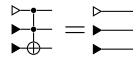
$\text{tof}(|001\rangle) = |001\rangle$ :



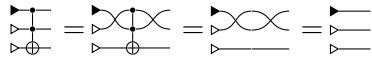
$\text{tof}(|010\rangle) = |010\rangle$ :



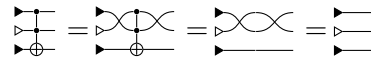
$\text{tof}(|011\rangle) = |011\rangle$ :



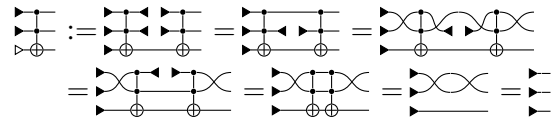
$\text{tof}(|100\rangle) = |100\rangle$ :



$\text{tof}(|101\rangle) = |101\rangle$ :



$\text{tof}(|110\rangle) = |111\rangle$ :



$\text{tof}(|111\rangle) = |110\rangle$ :

